TEES Chapter 8

SPECIFICATIONS FOR CHANGEABLE MESSAGE SIGN SYSTEMS



Department of Transportation



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CHAPTER 8

SPECIFICATIONS FOR MODEL 500, 510, AND 520 CHANGEABLE MESSAGE SIGN SYSTEMS

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SECTION 1 - GENERAL

- 8.1.1 The Models 500 and 510 CMS shall have a configuration of 5 Pixel Matrix Modules high by 12 Pixel Matrix Modules across (60 modules) forming a continuous matrix display of 96 pixels across by 25 pixels high (2400 pixels).
- 8.1.2 The Model 520 shall have a configuration of 5 Pixel Matrix Modules high by 6 Pixel Matrix Modules across (30 modules) forming a continuous matrix display of 48 pixels across by 25 pixels high (1200 pixels).
- 8.1.3 The Models 500 and 510 CMS shall have a Control Compartment on the front right side of its housing. The Control Compartment shall be accessible through panels on the sign's right end side and the compartment front door.
- 8.1.4 The Model 520 shall have a Control Compartment on the rear side of its housing. The Control Compartment shall be accessible through the compartment back door.
- 8.1.5 The Models 500 and 510 CMS systems shall be designed to operate on a Single Phase 120/240VAC Service, rated at 20KVA (minimum) where Xenon pixels are used, or 5KVA (minimum) where LED pixels are used.
- 8.1.6 The Model 520 CMS system shall be designed to operate on a Single Phase, 120/240 VAC Service, rated at 10KVA (minimum) where Xenon pixels are used, or 5KVA (minimum) where LED pixels are used.
- 8.1.6 The total weight of the supplied CMS shall not exceed:

Model 500 - 2400 pounds (1089.6kg) Model 510 - 2000 pounds (908kg) Model 520 - 1000 pounds (454kg)

A verification of the total weight shall be documented and included in the Certificate of Compliance.

8.1.8 ITEMS SUPPLIED

- 8.1.8.1 The CMS System shall include the Model 500 CMS or Model 510 CMS or Model 520 CMS as per contract.
- 8.1.8.2 Controller, Controller Cabinet, Controller Isolation Assembly, CMS Harnesses #4 and #5 and all other required equipment and wiring necessary to operate the system. The Controller shall be State Furnished unless called out in Contract Special Provisions.
- 8.1.8.3 Step Down Transformer(s) where Xenon pixels are used:

Model 500 & 510 - Four - 5KVA (one per line) - 120VAC to 20VAC Model 520 - Two - 5KVA (one per line) - 120VAC to 20VAC

8.1.8.4 Step Down Transformer where LED pixels are used:

Model 500, 510 - One - 5KVA - 120VAC to 20VAC Model 520 - One - 5KVA - 120VAC to 20VAC

- 8.1.9 CMS HARNESSES
 - 8.1.9.1 A Harness No. 1 shall be provided between each PMM connector assembly (CA and CB connectors) and its associated PDM CC connector in the associated PXDA.
 - 8.1.9.1.1 Each harness shall consist of: Required conductor quantity

No. 22 AWG or larger conductors

1 CAS Connector1 CBS Connector1 CCP Connector

- 8.1.9.1.2 Each harness shall be routed in properly supported cable trays.
- 8.1.9.2 A Harness No. 2 shall be provided between each PXDA CD connector and the associated PDA No. 4 CE connector.
 - 8.1.9.2.1 Each harness shall consist of: Required conductor quantity

No. 22 AWG or larger conductors

1 CDS Connector1 CES Connector

- 8.1.9.3 A Harness No. 3 shall be provided between the PDA No. 4 CF connector and the associated Controller Interface Terminal Block Positions on the CIP.
 - 8.1.9.3.1 The harness shall consist of: Required conductor quantity

No. 22 AWG or larger conductors

1 CFS Connector

Conductors-Stripped and Tinned

- 8.1.9.4 A Harness No. 4 shall be provided to interconnect the CMS CIP Panel Terminal Position to the Controller Cabinet CIA Assembly (via C8S and C9S connectors).
 - 8.1.9.4.1 The harness shall consist of: 300 feet (91.46m) of Atlas Cable,

Type A-881or equal

(24 pr. #18 AWG, multicolored pairs)

1 C8S Connector 1 C9S Connector

Logic Signal & DC Logic Ground Conductors - Stripped and tinned

8.1.9.5 A Harness No. 5 shall be provided to interconnect the CMS CIP Panel Terminal Positions (Current Monitor Circuits) to the TB1 in the Model 334C Controller Cabinet.

8.1.9.5.1 The harness shall consist of: 300 feet (91.46m) of Atlas Cable,

Type A-881or equal

(6 pr. #18 AWG, multicolored pairs) Conductors - Stripped and tinned

- 8.1.9.6 The harnesses #4 and #5 shall be neatly rolled up on a wooden reel and prepared for shipping. It shall be so designated and labeled with the associated CMS.
- 8.1.9.7 All harnesses shall have a minimum of 2 feet (0.610m) of slack available on each connector or connector assembly end and shall be permanently labeled according to connector pin assignments, device addressing and/or function.
- 8.1.9.8 Harnesses #1 and #2 shall provide enough slack, to completely pull the PXDAs out of the equipment rack, to facilitate maintenance.

8.1.10 SPARE PARTS

Spares parts shall meet the requirements of these specifications. The following items shall be included per each delivered Model 500, 510 or 520 CMS system:

2 each - Pixel Matrix Module with pixels (PMM)

5 each - Pixel Driver Module (PDM)

1 each - Pixel Driver Assembly (PXDA)

1 each - CMS Isolation Module

1 each - Controller Isolation Assembly (CIA)

SECTION 2 - PIXEL MATRIX MODULE

8.2.1 GENERAL

- 8.2.1.1 Each PMM shall consist of 40 Pixels (Xenon lamps in associated lamp sockets and reflectors or LED pixels), module panel, 4 TSD No. 2, and Connectors CAP and CBP. Each PMM shall be interchangeable.
- 8.2.1.2 Each PMM shall be secured to the sign support framing at each of its four corners by the TSD No. 2 Devices.
- 8.2.1.3 All PMM wiring shall be No. 22 AWG or larger.

8.2.2 FORMED PLASTIC COVER

8.2.2.1 Formed Plastic Cover (FPC) shall be coated or treated to reduce glare off of PMM.

The coating or treatment shall not diminish light output by more than 15%.

The outside of the FPC shall be smooth.

The FPC shall maintain a watertight seal against the PMM panel.

823 PIXELS

8.2.3.1 OPTION 1 - XENON

MODELS 500, 510 & 520

Each lamp shall be CHICAGO MINIATURE Type 1524X or THHC Type 2472X-2, or equal (rated at 7.2 watts at 24 VAC).

The Reflector can either be individual for each pixel or a mold for the entire Pixel Matrix Module. The PMM shall be sealed to prevent any water or dust from coming in direct contact with the reflector or the lamp.

Each lamp socket shall be a brass wedge base rated 20W at 28 VAC with tabs to accommodate PMM wiring.

Each Lamp and Socket shall be rated for outdoor usage, direct weather environment and vibration resistant.

8.2.3.2 OPTION 2 – LIGHT EMITTING DIODE (LED)

MODELS 500, 510 & 520

8.2.3.2.1 GENERAL AND MECHANICAL

Each pixel shall consist of light emitting diodes (LED's).

The maximum weight of a LED PMM shall be 7 lbs. (3.178kg).

Pixel Matrix Modules shall conform to all applicable dimensions as specified in details sections 8-8, 8-9 and 8-10.

Each pixel panel module shall have the manufacturer's name, trademark, model number, serial number permanently marked on the back of the panel. Rated voltage, current, power consumption and volt-amperes (VA) shall also be identified.

The PMM's shall be rated for a minimum useful life of 48 months.

The maximum luminosity degradation shall be less than 15% after 48 month of operation.

All PMM's shall meet all parameters of this specification during this period.

8.2.3.2.2 LED PIXEL TYPE 1

Each LED pixel shall be mounted in a 44.5 mm (1.75 in) (diameter or on a side) x 44.9 mm (1.77 in) (maximum dimensions) length hooded visor shell as shown in Fig 8-8-9.

Each LED pixel shall be mounted 6 degree down off-axis mounting plane (vertical).

The shape of an individual pixel shall be square or circular and the LED's shall be evenly distributed in the pixel.

Each LED pixel (measured from LED to LED on the outside) shall be no larger than 38 mm (1.5 in.) (either diameter or on a side).

Each LED pixel shall be encapsulated for water resistance.

Each LED pixel shall be attached to the module panel using a single crew (M-3) mounting with locating pin

Each pixel shall have two 300 mm (12 in.) leads for electrical connection.

The wiring shall be color coded per TEES Section 1.3.13.6.

The wiring shall be fixed to the back of the PMM to prevent snagging during installation or removal per TEES Section 1.3.13.3.

Electronics shall be encapsulated in each pixel, or covered if a separate, intermediated PC board is used.

8.2.3.2.3 LED PIXEL TYPE 2

Each LED pixel shall consist of an array of LED's mounted on a PC board as shown in Fig 8-8-10.

Entire Pixel Matrix Module shall be sealed and coated for water resistance.

The shape of an individual pixel shall be square or circular and the LED's shall be evenly distributed in the pixel.

Each LED pixel shall be no larger than 38 mm (1.5 in.) (either diameter or on a side).

The electronics on the PC board shall not be visible looking through the FPC except for the LED's.

Wiring and electronics on the back of the PC board shall be covered.

8.2.3.2.4 LIGHT EMITTING DIODES

LED shall be ultra-bright type, AlInGaP

Color shall be yellow, $592 \pm 5 \text{ nm}$

All LED's for each pixel shall be from the same manufacturer and same color bin.

The MTBF of the LED devices should exceed 100,000 hrs of operation.

8.2.3.2.5 PHOTOMETRIC REQUIREMENTS

All measurements shall be performed after environmental testing, 24 hours each at –40 0 C (-40 0 F) and 74 0 C (165.2 0 F), and at 25 0 C (77 0 F) (normal operating temperature).

Each pixel shall have a minimum initial brightness of 40 candela, at full bright dimming level (level 7), over the temperature range of -40 °C to +74 °C (-40 °F to 165.2 °F).

Each pixel shall maintain a brightness of 34 candela throughout the minimum useful life of 48 months at a temperature of 25 °C (77 °F).

Each pixel shall have a viewing angle of 30 degrees or more.

A failure of one LED shall not result in a loss of more than 25% of light output

8.2.3.2.6 POWER AND CONTROL

Maximum power consumption shall be 2.0 watts per pixel.

Each pixel shall accept 18 VAC to 28 VAC (22 VAC nominal) as input voltage.

The minimum power factor shall be 0.95.

Each LED pixel shall be compatible with existing PDM (see TEES Section 8.4.6). Each pixel shall respond to dimming control using the stepped triac modes (pulse width modulation) as defined in TEES Section 8.6.2.

8.2.4 MODULE PANEL

The panel shall be fabricated from 1.588mm (0.0625 inch) minimum thick aluminum sheet. After fabrication the panel and support shall be treated with a black color according to Section 5 - Structures and Housing Surface Treatment Requirements.

SECTION 3 - SIGN STRUCTURE AND HOUSING

8.3.1 HOUSING REQUIREMENTS

8.3.1.1.1 The housing shall include, but not be limited to, the following:

Doors Ventilation Latches/Handles Gasketing

Hinges and Door Catches Cage Supports and Mounting for PMMs and

CMS Equipment

8.3.1.2 HOUSING CONTRUCTION

- 8.3.1.2.1 The CMS housing shall be constructed to present a clean, neat appearance.
- 8.3.1.2.2 The CMS housing shall be fabricated from 0.125-inch (3.17mm) minimum thick 5052-H32 aluminum alloy and shall be designed to withstand 33 pounds (14.97kg) per square foot as specified in the latest AASHTO publication entitled "Standard Specification for Structural Supports for Highway Signs, Luminaries and Traffic Signals".
- 8.3.1.2.3 The CMS housing shall have interior cage support frames to mount the PMMs. The cage support frame shall withstand or minimize vibration when the sign is mounted with any number of PMMs.
- 8.3.1.2.4 The doors, lifting eyes, gasket channels, dual tracks and all supports welded to the housing shall be fabricated of 0.125 inch (3.17mm) minimum thickness aluminum sheet. Bolted on supports shall be either the same material and thickness as the housing or 0.105 inch (2.67mm) minimum steel sheet.
- 8.3.1.2.5 The exterior Z-bar supports shall be fabricated of 0.250 inch (6.35mm) minimum thickness and the material shall be the same as the CMS housing.

MODEL 500

8.3.1.2.6 A 6 inch (152.4mm) wide border made of the same material as the housing shall encompass the Pixel Matrix Module Section/Array and enclose any open areas around the section/array. The border shall be uniform and painted or treated according to specifications under this section.

MODEL 510 & 520

8.3.1.2.7 A 5-inch (127mm) wide border made of the same material as the housing shall encompass the Pixel Matrix Module Section/Array and enclose any open areas around the section/array. The border shall be uniform and painted or treated according to specifications under this section.

8.3.1.3 WELDING

- 8.3.1.3.1 All exterior seams shall be continuously welded and each weld shall be uniform flow.
- 8.3.1.3.2 Welding on aluminum housings shall be done as follows:
- 8.3.1.3.2.1 On all exterior seams the gas tungsten arc (TIG) process using bare aluminum welding electrodes shall only be used.
- 8.3.1.3.2.2 On all interior seams the gas metal arc (MIG) or gas tungsten arc (TIG) process using bare aluminum welding electrodes shall be used.
- 8.3.1.3.3 The aluminum welding electrodes shall conform to the requirements of the American Welding Society (AWS) A5.10 for ER5356 aluminum alloy bare welding electrodes. Procedures, welders and welding operators for welding on aluminum shall be qualified in accordance with the requirements of AWS B3.0, "Welding Procedure and Performance Qualification", and to the practices recommended in AWS C5.6.
- 8.3.1.3.4 The housing front door frames shall be double flanged out on all 4 sides and shall have strikers to hold tension on and form a firm seal between the door gasketing and the frame. The dimension between the door edge and the housing external surface when the door is closed and latched shall be 0.156 ± 0.08 inch $(3.96\text{mm} \pm 2.03\text{mm})$.
- 8.3.1.3.5 Gasketing shall be provided on all door openings and shall be dust-tight. Gaskets shall be 0.25 inch (6.35mm) minimum thickness closed cell neoprene or silicone (BOYD R-10480 or equal) and shall be permanently bonded to the metal. If neoprene is used the mating surface of the gasketing shall be covered with a silicone lubricant to prevent sticking to the mating metal surface. A gasket top channel shall be provided to support the top gasket on the door (prevent gasket gravitational fatigue).

8.3.1.4 ACCESS DOOR AND PANELS

8.3.1.4.1 FRONT ACCESS DOOR

MODEL 500

8.31.4.1.1 For the Model 500, the latching handle shall be on the left side of the front door with hinges on the right side. The latching handle shall be turned clockwise to an angle of 90 ± 5 degrees from the closed position in order to open the door. Latching the door closed from the open position shall be only performed in the counterclockwise direction.

MODEL 510 & 520

- 8.3.1.4.1.2 The Models 510 and 520 shall have double compartment doors. The latching|handle of the right door shall be on the left side with hinges on the right side. The latching handle shall be turned clockwise to an angle of 90 ± 5 degrees from the closed position in order to open the door. Latching the door closed from the open position shall be only performed in the counterclockwise direction. The left door shall be a mirror image of the right door.
- 8.3.1.4.1.3 The latching handle shall have provision for padlocking in the closed position. Each handle shall be 0.75 inch (19.05mm) minimum diameter stainless steel with a minimum 0.5 inch (12.7mm) shank. The padlocking attachment shall be placed at 4.0 to 5.0 inches (101.6mm to 127.0mm) minimum gripping length shall be provided.
- 8.3.1.4.1.4 The latching mechanism shall be a three-point draw roller type. The pushrods shall be turned edgewise at the outward supports and have a cross section of 0.25 inch (6.35mm) thick by 0.75 inch (19.05mm) wide, minimum.
- 8.3.1.4.1.5 Two-bolt per leave hinges shall be provided to bolt the CMS Housing (at the Control Compartment Section) to the doors. The housing shall have 4 hinges per door. Each hinge shall be 3.5 inches (88.9mm) minimum length and have a fixed pin. The pin ends shall be welded to the hinge and ground smooth. The pins and bolts shall be covered by the door edge and not accessible when the door is closed.
- 8.3.1.4.1.6 The door shall be provided with catches to hold the door open at both 90 and 180±10 degrees. The catch minimum diameter shall be either 0.375 inch (9.53mm) for plated steel or aluminum rods or 0.25 inch (6.35mm) for stainless steel. The catches shall be capable of holding the door open at 90 degrees in a 80-mph (128.82 km/h) wind acting at an angle perpendicular to the plane of the door.

8.3.1.4.2 SIDE ACCESS PANELS

- 8.3.1.4.2.1 There shall be two side access panels (top and bottom) for the Model 500 and one side access panel for the Model 510 on the CMS housing right end side adjacent to the Control Compartment. The panels shall be detailed as per Appendix Detail. The panels shall have louvered vents as specified under these specifications.
- 8.3.1.5 The CMS housing shall be provided with 2 lifting eyes Models 500 and 510, and 1 lifting eye Model 520 to be used when placing the housing on the sign structure. The lifting eyes shall be located where required. Each eye shall have a minimum diameter of 1.00 inch (25.4mm). The fully assembled sign shall have sufficient structural strength to be lifted and moved by either lifting eye, or both lifting eyes, without damage or permanent deformation to any part of the sign. A label shall be attached near each lifting eye reading, "Lift Vertically to Prevent Damage".

8.3.2 SURFACE TREATMENT

- 8.3.2.1 The Contractor shall have the option of Surface Treatment and Painting described herein or Anodizing:
- 8.3.2.2 Alternative surface treatment and painting methods must equal or exceed those specified and be approved by CALTRANS Division of Material Engineer Testing Services (METS) Chemical Branch.

8.3.2.3 ANODIZING

- An anodic coating shall be applied to the aluminum surface after the surface has been cleaned and etched. The cleaning and etching procedure shall be to immerse in inhibited alkaline cleaner at 71° C for 5 minutes (Oakite 61A, Diversey 909 or equivalent in mix of 6 to 8 ounces per gallon (45.26gram to 60.34gram per liter) to distilled water). Rinse in cold water. Etch in a sodium solution at 66° C for 5 minutes (0.5 ounce (14.18gram) sodium fluoride plus 5 ounces (141.75gram) of sodium hydroxide mix per gallon (3.79liter) to distilled water). Rinse in cold water. Desmut in a 50% by volume nitric acid solution at 20° C for 2 minutes. Rinse in cold water.
- 8.3.2.3.2 The anodic coating shall conform to MIL-A-8625C (Anodic coatings for Aluminum and Aluminum Alloys) for Architectural Class II A34 Coating with flat black dye required on the CMS front surface. The black surface coating shall have a design life of a minimum of 10 years.
- 8.3.2.3.3 The anodic coating shall be sealed in a 5% aqueous solution of nickel acetate (pH 5.0 to 6.5) for 15 minutes at 99° C.
- 8.3.2.3.4 Black anodic coating shall be applied to the 6-inch border on the entire front face of the CMS housing, screen assembly frames, the 60 PMM panels and front access door. All other surfaces shall be treated with clear anodic coating.

8.3.2.4 SURFACE TREATMENT FOR PAINTING

The CMS unit assemblies such as the PMM panels, front and side access doors, etc. shall be treated prior to painting. The treatment steps are as follows:

1. Surface treat aluminum in accordance with the provisions of ASTM D-1730, Type B, Method 6 or Method 7. Immediately prior to treatment, the aluminum surfaces shall be cleaned following the procedures in either ASTM D-1730, Type A, Method 3 or ASTM D-1730, Type B, Method 1.

833 PAINTING

8.3.3.1 GENERAL

8.3.3.1.1 After applications of the primer all exposed area of primed surfaces shall receive a minimum of 2 finish coats of paint conforming to the requirements for White Tintable Finish Paint Waterborne, Formula PWB-164, Revised or exterior grade latex paint conforming to the following:

Property	Value	ASTM Designation
Pigment content,%	24 Max	D3723
Nonvolatile content Wt%	49 Min.	D2369
Viscosity, KU	75 Min. to 90 Max.	D 562
Fineness of grind	6 Min.	D1210
Hegman		
Drying time at 77° F		D1640
50% RH, 4 mil wet film		
Set to touch, minutes	30 Max.	
Dry through, hours	1 Max.	
Fineness of grind Hegman Drying time at 77° F 50% RH, 4 mil wet film Set to touch, minutes	6 Min. 30 Max.	D1210

No visible color change in the finish coats shall occur when tested according to ASTM Designation: G 53 using FS 40 UV- B bulbs for a min. of 38 cycles. The cycle shall be 4 hours UV exposure at 60° C and 4 hours condensate exposure at 40° C. The vehicle shall consist of 100% acrylic latex with a minimum of necessary additives.

8.3.3.1.2 Individual batches/lots of paints will require testing by the METS – Chemical Branch to assure compliance with specification requirements prior to use unless otherwise approved by the engineer. 8in x 8in (203.2mm x 203.2mm) sample panels coated according to the requirements of these specifications and the following shall be provided for testing and approval with 90 days of award of contract. Two sample panels shall be provided for each color of finish paint. Each color panel shall be the same type aluminum specified to receive that color finish. When more than one type aluminum is required 2 panels of each color on each type shall be submitted.

8.3.3.2 APPLICATION

8.3.3.2.1 After a complete Surface Treatment process, one coat of vinyl wash primer with one part acid and four parts resin shall be applied. One part of acid is to be slowly added with constant stirring to four parts by volume of the resin just before use. The mixed components must be used within 8 hours. Vinyl wash primer is formulated for spray application only and shall be applied to a dry film thickness of 0.3 to 0.5 mils.

8.3.3.2.3	After application of the vinyl wash primer all exposed surfaces shall receive a minimum
	of 2 finish coats of an exterior grade acrylic latex paint. The first finish coat shall be
	tinted by the manufacturer to provide a color to contrast with the final finish coat. The
	total dry film of the first finish coat shall be not less than 2 mils.

- 8.3.3.3 THE SECOND FINES COAT COLER SHALL MATCH THE FOLLOWING:
- 8.3.3.3.1 Federal Standard 595a No. 37038 to be applied to the 6-inch (152.4mm) border on the front face of the CMS housing including, screen assemblies Control Cabinet Door and the 60 Pixel Matrix Module Panels.
- 8.3.3.3.2 Federal Standard 595a No. 37769 to be applied to the CMS housing including Z-Bars.
- 8.3.3.3.3 The total dry film thickness of all applications of the second finish coat shall be not less than 2 mils or more than 4 mils.
- 8.3.3.3.4 The 2 finish coats shall be applied in 2 or more applications to a total dry film thickness of not less than 4 mils or more than 8 mils.
- 8.3.3.3.5 The total dry film thickness of all paint applications shall be not less than 4.3 mils or more than 8.5 mils.
- 8.3.3.3.6 A minimum drying time of 12 hours shall be allowed between finish coats.
- 8.3.3.3.7 After finish paint has cured adequately, the adhesion of the coating to the substrate when tested according to ASTM Designation: D 3359, Test Method A shall be 4A or higher.

8.3.4 VENTILATION

- 8.3.4.1 The CMS housing shall be provided with the necessary louvered vents on both sides of the housing and below the housing to provide sufficient ventilation as described below.
- 8.3.4.2 There shall be no active ventilation such as electric fans or other devices in the CMS housing except in the CMS Control Compartment.
- 8.3.4.3 The CMS Control Compartment shall have a ventilation system including intake, exhaust, filtration, fan assembly and environmental control. A louvered vent of 0.25 inches (6.35mm) in depth maximum shall be provided and positioned near the bottom on the lower panel of the CMS Sign Housing rightside. A removable and reusable filter filting in a fan mounting shell shall cover the vents and shall be held in place with bottom and side brackets and a spring-loaded top clamp.
- 8.3.4.3.1 The intake (including filter) and exhaust areas shall pass a minimum of 60 cubic feet (1.69 m^3) of air per minute.
- 8.3.4.3.2 The fan assembly shall have ball or roller bearings and a capacity of at least 60 cubic feet (1.69 m³) of free air delivery per minute.

- 8.3.4.3.3 The fan assembly shall be a thermostatically controlled device adjustable to turn ON between 33° C and 65° C with a differential of not more than 6° C between automatic turn ON and OFF. Thermostat indication shall be marked in 10° C increments. Manual adjustment shall be provided.
- 8.3.4.3.4 The filter material shall be a McMaster-Carr Permanent Washable Air Filter No. 2069K12 or equal.
- 8.3.4.3.5 The fan assembly shall be mounted on the CMS Control Compartment lower right side and exhaust into the Pixel Matrix Module Section.

SECTION 4 - CONTROL COMPARTMENT

8.4.1 GENERAL

8.4.1.1 CMS Control Equipment and associated wiring shall be furnished and installed in the Control Compartment. The equipment shall consist of:

One - CMS Interface Panel (CIP)

One - Power Distribution Assembly No.4 (PDA No.4)

Five - Pixel Driver Assemblies (PXDAs)

MODEL 500 & 510

Sixty - Pixel Driver Modules (PDM) Where Xenon Pixels are used, Four - 5 KVA Transformers Where LED Pixels are used, One - 5 KVA Transformer

MODEL 520

Thirty - Pixel Driver Modules (PDM)
Where Xenon Pixels are used, Two - 5KVA Transformers
Where LED Pixels are used, One - 5 KVA Transformer

- 8.4.1.2 All panels and assemblies shall be mounted on the 19-inch (482.6mm) Rack.
- 8.4.1.3 All circuit breakers, fuses, switches and indicators shall be readily visible and accessible when the CMS Control Compartment Front Door is open except for the PDM fuses which shall be located near the PDM connector.
- 8.4.1.4 All equipment when called out shall be clearly and permanently labeled. Marking strips shall be made of material that can be easily and legibly written on using a pencil or ballpoint pen. Marker strips shall be located immediately below the item to be identified and must be clearly visible with the item installed.
- 8.4.1.5 The assembly or panel depth dimension shall include the terminal blocks and assembly mounted part of the connectors.
- 8.4.1.6 All assemblies and panels shall allow air circulation through its top and bottom unless specifically called out otherwise.
- 8.4.1.7 Assemblies shall be fabricated of 0.0625-inch (1.59mm) minimum thickness aluminum sheet with metal surface treated with either clear chromate or anodized.

8.4.2 19-INCH EIA RACKS

- 8.4.2.1 The 482.6mm (19-inch) EIA Rack shall be installed a minimum of 25.4mm (1.0 inch) inside the Control Compartment Front Door for mounting of the Control Equipment.
- 8.4.2.2 The 482.6mm (19-inch) EIA Rack shall consist of a pair of continuous adjustable equipment mounting angles. The angle nominal thickness shall be either 3.42mm (0.1345 inch) plated steel or 2.67mm (0.105") stainless steel. The angles shall be tapped with 10 32 threads with EIA universal spacing. The angle shall comply with Standard EIA RS-310-B and shall be supported at the top and bottom.
- 8.4.2.3 Clearance between the mounting angles for mounting equipment shall be 450.85mm (17.75 inches).
- 8.4.2.4 The Model 500 vertical mounting surface shall be a minimum of 1689.10mm (66.5 inch) and the Models 510 and 520 vertical mounting surface shall be a minimum of 1168.4mm (46 inch).
- 8.4.2.5 The top of the mounting area to the center of the first screw hole shall be a minimum of 9.53mm (0.375 inch).

8.4.3 CMS INTERFACE PANEL (CIP)

- 8.4.3.1 The CIP mounting surface used for required equipment shall be recessed if necessary so that the equipment front surface is even with the CIP rack mounting front surface. The CIP shall have a maximum depth of 203.2mm (8 inch).
- 8.4.3.2 Service equipment and conductors shall be separated and isolated from the Controller Interface Terminal Blocks and Harness No.3.
- 8.4.3.3 A Twenty (20) Position minimum AC- Copper Bus(es) shall be provided. Two (2) 1/0 AWG wiring lugs shall be available on the Bus for AC Neutral Service Termination. The Bus shall have minimum ampacity capability of 400 Amperes. Nylon screws or standoffs shall be used to provide 500 megohms isolation between AC- and Equipment Ground & DC Logic Ground when tested at 250 VDC.
- 8.4.3.4 A Two (2) Position termination lug shall be provided for Equipment Ground on the CIP. A #10 AWG Conductor shall be routed from the CMS frame termination to the CIP and from the CIP to PDA #4, T2 Block Position #8.
- 8.4.3.5 The Equipment Ground shall be connected directly to the CMS Frame.
- Where Xenon Pixels are used, a 4-Pole 50 Ampere minimum rated circuit breaker shall be installed on the panel and designated for CMS Main Disconnect.
- Where LED Pixels are used, a 2-Pole 30 Ampere minimum rated circuit breaker shall be installed on the panel and designated for CMS Main Disconnect.

- 8.4.3.8 Disconnect Line and Load Side Lugs shall accommodate No. 1/0 AWG Conductors.
- Where Xenon Pixels are use, thirty 1 Pole 20 Ampere PXDA Circuit Breakers shall be furnished and installed to protect the PXDA Busses, 6 per PXDA. The Breakers shall be split into 15 breakers per lines L1 and L2 (1st 120 VAC leg) and 15 breakers per lines L3 & L4 (2nd 120 VAC leg). See detail 8-8-3 for Assignments.
- Where LED Pixels are used, thirty 1 Pole 20 Ampere PXDA Circuit Breakers shall be furnished and installed to protect the PXDA Busses, 6 per PXDA. The Breakers shall be split into 15 breakers per lines L1 and L2 (120 VAC leg). See detail 8-8-3 & 8-8-4 for Assignments.

8.4.3.11 CONTROLLER INTERFACE

- 8.4.3.11.1 A 44 Position Phoenix Contact, Type MBK 2.5/E, DIN Terminal Block or equal (CITB) shall be installed on the panel for CMS Harnesses #3, #4 and #5 termination.
- 8.4.3.11.2 CITB Terminals shall accommodate No.18 AWG conductors. Strain relief shall be provided for incoming Harnesses #4 & 5 Cable(s).
- 8.4.4 POWER DISTRIBUTION ASSEMBLY No. 4 (PDA No. 4)
- 8.4.4.1 The PDA No. 4 shall meet all the requirements of the Power Distribution Assembly as specified in the TSCES (Chapter 6) including details as applicable except for the following:
- 8.4.4.1.1 The PDA No. 4 shall consist of:
 - 1 -- Duplex NEMA 5-15R Equipment Receptacle with GFI
 - 1 -- 1 Pole 15 Amperes, 120 VAC Equipment Circuit Breaker
 - 1 -- 1 Pole 15 Amperes, 120 VAC Main (includes Model 206 and Temp Control) Circuit Breaker
 - 1 -- Model 206 Power Supply Module (+24 VDC) and Socket
 - 1 -- 10 Position Terminal Block T2
 - 1 -- 4 Position Terminal Block T3
 - 1 -- CFP Connector
 - 5 -- CEP Connector
 - 1 -- CMS Isolation Module (CMS ISO MOD) and Socket Connector
- 8.4.4.1.2 Retaining Devices shall be provided mounted above the ISO Module and Model 206 Power Supply Module. Each device when locked down shall prevent the module from backing out of its connector. When not in use the device shall rest in a position allowing the module removal or insertion.
- 8.4.4.2 CMS Isolation Module (CMS ISO MOD)

- 8.4.4.2.1 The module shall optically isolate, receive/drive and condition DC Logic Signals from the Controller Cabinet (CIA) complying to RS 423 Standards at a minimum distance of 152.40m (500 Feet).
- 8.4.4.2.2 The total module load current shall not exceed 100 mA of +24 VDC power. Logic Voltage between the ISO Module and the LDMs shall be +12 VDC. The CIA Assembly shall supply the power for interface circuitry between the ISO Module and the CIA.
- 8.4.4.2.3 The front panel shall be fabricated of 2.29mm (0.090 inch) minimum thickness aluminum or stainless steel sheet. A momentary toggle switch shall be provided on the module front panel for Sign Lamp Test Function. Five indicators shall be provided on the front panel of the module to indicate the Dimming Signal, Clear State, Clock Signal, Device Enable and PXDA Latched State.
- 8.4.4.2.4 The addressing of the PXDAs shall be implemented on the module by decoding the upper three binary bits 5, 6 and 7 of the address bus to generate the PXDA select lines designated 1 through 5 (See Appendix details).
- 8.4.4.3 The CMS ISO MOD and PDMs shall have exclusive use of the Model 206 Power Supply.
- 8.4.4.4 A Gas Tube type Surge Protector shall be provided across T2 terminal positions 8(EGND), 9(AC+), and 10(AC-).
- 8.4.5 PIXEL DRIVER ASSEMBLY (PXDA)
- 8.4.5.1 The assembly shall have a maximum depth of 241.3mm (9.5 inch) (includes connector assemblies on rear of assembly).
- 8.4.5.2 The assembly shall intermate and support 12 PDMs, provide interface between the PDMs to the associated PMM, route AC and DC power to the PDMs and interface control signal lines to the PDMs.
- 8.4.5.3 The assembly shall have a PCB Motherboard for routing circuits to and from the lamp driver assembly connectors; PDM Connectors (1 through 12), CC Connectors (1 through 12), CD Connector, and the 6-position terminal blocks (or four 3 position).
- 8.4.5.3.1 Intercomponent wiring shall be by laminated copper clad track having a minimum weight of 4 ounces per square foot with adequate cross section for current to be carried.
- 8.4.5.3.2 All assembly connectors shall be mechanically supported to the frame of the assembly.
- 8.4.5.3.3 The PXDA-PDM connector shall be a 43/86S PCB Connector vertically centered. Guides, top and bottom, shall be provided to insert and support the PDM. The connector contacts shall start with pin 1 at the upper left when viewed from the PXDA front.

8.4.5.3.4 The AC+ and AC- Buses shall be routed across the PXDA from both sides (Terminal Block TBA1 assigned to PDM Connectors 7 to 12 and TBA2 assigned to PDM Connectors 1 to 6). The AC-Bus(s) and return shall be able to carry a minimum 60 Ampere Load. TB1 and TB2 blocks may be divided into four(4) 3 position Terminal Blocks, two for power and two for AC neutral.

8.4.6 PIXEL DRIVER MODULE (PDM)

- 8.4.6.1 GENERAL REQUIREMENTS
- 8.4.6.1.1 The module PCB connector shall be a 43/86P.
- 8.4.6.1.2 Each module shall be interchangeable without modification or alteration to the module connectors and/or circuitry.
- 8.4.6.1.3 One PDM shall exclusively control and operate one PMM.
- 8.4.6.1.4 Address lines incoming from the CMS ISO MOD (The 4-bit PDM Address lines and the associated PXDA Select line) shall be compared with the PDM slot address I.D. lines and PXDA Select I.D. lines on the module.
- 8.4.6.1.4.1 The slot address I.D. lines shall be decoded and implemented using hard-wire logic on the PXDA Motherboard adjacent to the PCB 43/86S connector. Five I.D. lines minimum shall be used as follows; the first four I.D. lines 1 to 4 shall be compared to the incoming PDM Address lines and the I.D. line 5 shall be compared to the incoming PXDA Select line.
- 8.4.6.1.4.2 Jumper wires, dip switches, and shunt jumpers shall not be used to decode and implement the hard-wire logic. Permanent solder traces with a resistor network or equal may be allowed.
- 8.4.6.1.5 The module shall be fused protected on the three incoming AC+ traces with three-5 Amperes Medium Blow Type Fuses. The Triac Load shall be evenly divided through the fuses and the three power buses (14, 13 and 13). The pattern shall be the same for all LDMs.
- 8.4.6.1.6 No more than 80 mA of +24 VDC power shall be used by each PDM.
- 8.4.6.2 FEATURE REQUIREMENTS
- 8.4.6.2.1 The module shall consist of serial shift registers with parallel and latched tri-state outputs, 40 optically coupled triac drivers, 40 pixel load triacs and support circuitry to interface and control DC logic control signals to an external pixel load.

8.4.6.2.2 Pixel Load Triac Ratings:

- 1. 8 AMP On- State RMS Current
- 2. 40 AMP Peak Surge Current for 1 full AC Cycle
- 3. 400 Volt RMS Blocking Voltage minimum
- 4. Maximum 1 ma Leakage Current either direction
- 5. Maximum 3 us Turn On Time
- 6. Maximum 5 AMP DC Gate Trigger Current
- 7. The Triac shall not be a sensitive gate type

SECTION 5 - MODEL 500, 510 and 520 CMS SYSTEM INTERCONNECTION

8.5.1 GENERAL

- 8.5.1.1 The CMS System consists of the Model 500, 510 or 520 CMS, Harnesses No. 4 and No. 5 Interconnect, the CIA resident in the Controller Cabinet and the Controller. This section shall detail the interface and control requirements that mate the controller to the CMS.
- A systems check shall consist of running the Caltrans' DAT CMS (Diagnostic and Acceptance Testing for the Changeable Message Sign) Software Program with the CMS (Complete interconnection and interface of CMS, Controller, CIA, Controller Cabinet and associated equipment).
- 8.5.1.3 Prior to the delivery of the Model 500, 510 or 520 CMS, a verification certificate shall be submitted to the Engineer. The certificate shall show that a systems check has been performed and the system has functioned properly under the Engineer's guidelines.
- 8.5.1.4 CIA AC+ power shall be connected to cabinet PDA #3 via a 3-foot (9.15m) NEMA Type 5-15P grounding type plug. The controller duplex receptacle shall be used for power source.

8.5.2 CMS CONTROL ISOLATION ASSEMBLY (CIA)

- 8.5.2.1 The CIA shall function as the isolation / driver interface between the Controller and the CMS, Sense light and AC voltage levels, sense the 4 power line current levels, read the Controllers Dim Level Select Lines and via selected level points control the Triac Turn On Point and provide CMS Soft Start Feature.
- 8.5.2.2 The CIA shall optically isolate, receive / EIA drive and condition DC Logic Signals to and from the CMS ISO Module complying to 423 Standards at a minimum of 500 ft (152.40m). The system shall be capable of writing to all 60 PDMs within 100 ms.
- 8.5.2.3 The CIA shall sense the 5 CIA Control Lines (Line 4 is reserved) and react as follows:

CIA CONTROL LINES	FUNCTION	RANGE
3 2 1 5		
000 0*	Power Line 1	0 - 100 AMPs *
0 0 1 0*	Power Line 2	0 - 100 AMPs *
0 1 0 0*	Power Line 3	0 - 100 AMPs *
0 1 1 0*	Power Line 4	0 - 100 AMPs *
1 0 0	Photo Cell	0 - 5 VDC
1 0 1	AC+ Line	75 - 150 VAC
1 1 0	Test	+5 VDC
1 1 1	Test	0 VDC

- Line 5 Logic 1 shall decode Power Line Range to 0 10 AMPs
- 8.5.2.3.1 The selected function shall present to the controller via the 8 Sense Lines its range in 256 increments (binary with Line 1 equal to the least significate bit). The Values shall be linear over the range. Sense output shall be compatible with the controller unit. The A/D convertor shall be a ADC 804 or equal with conversion triggered by a CLOCK Input. Conversion shall be completed within 250 us.
- 8.5.2.4 DIM Control shall be as called out under Specifications 2.4. The CIA shall read the Photo Cell Sensor Output and the incoming AC Voltage report to the controller. The controller shall decode the Dim Control Level via the three DL Lines as Follows:

LINES	FUNCTION
1 2 3	
000	Turn Sign Off, NO ON PULSE
100	Adjustment Level 1 (Lowest intensity Level)
010 to 110	Adj. Levels 2 to 6
111	Adjustment Level 7 (Highest intensity Level)

- 8.5.2.4.1 The selected level shall be matched to a DIM Control Phase Triggering Point adjustable by an individual 10 turn (min.) discrete potentiometer accessible from the front panel The Phase Triggering Pulse shall be mirrored back to the controller.
- 8.5.2.5 Four toroidal current monitoring devices (BICRON ELECTRONICS B5303 or equal) installed on the secondary side on each of the Four Power Lines (CMS Main Disconnect) shall input via C10 Connector to the current sensing circuitry and upon request to the A/D Convertor.
- 8.5.2.6 The CIA shall upon request run a self check on the A/D Convertor Sense Lines. A +5 VDC Request shall provide a "FF" Response. A 0 VDC Request shall provide a "00" Response.

SECTION 6 - CONTROLLER CABINET

8.6.1 334 CABINET

- 8.6.1.1 The Controller Cabinet shall comply with the Transportation Electrical Equipment Specifications (TEES), Chapter 1, dated August 16, 2002; and the TSCES, Model 334 Cabinet Specifications, Chapter 6, dated January 1989 with the following amendments:
- 8.6.1.2 Amend Specification 6.1.1.3 "Model 334 Cabinet (Ramp Meter)" to read "Model 334C Cabinet".
- 8.6.1.3 Amend Input Panel #3 detail in TSCES-9 plan sheet with the details in Appendix A called "Model 334C Cabinet, Input File No. 3-Upper Part" and "Model 334C Cabinet, Input File No. 3-Lower Part".
- 8.6.1.4 At the Contractor's Option, DIN Terminal Block conforming to TB1 Block type may be provided in lieu of TB2 to TB6 Terminal Blocks and EG Buses. The terminal position requirements shall match Input Panel #3 needs. EG Segments shall be located between Detector connection segments for field cable shield grounding. The DIN Railing shall be located vertical, centered on the panel. Spade Lugs are not required with this option.
- 8.6.1.5 Amend "Model 208 Monitor Unit Pin Assignment" detail in TEES, dated August 16, 2002 plan sheet as follows:

MODEL 208 MONITOR UNIT PIN ASSIGNMENT

PIN	FUNCTION
1/44	DC Ground
2/43	WDT Ext. Reset
5/40	WDT IN
10/35	+24 VDC
15/30	AC-
17/28	Normally Open, Circ. #2
19/26	AC+
20/25	Normally Closed, Circ. #1
21/24	Circ. Common #1 & #2
22/23	WDT Lamp (External)

NOTE:

Card connector keyed between pins 2 & 3, and pins 11 & 12.

8.6.2 LIGHT LEVEL CONTROL SYSTEM

8.6.2.1 GENERAL REQUIREMENTS

- 8.6.2.1.1 The Light Level Control System shall consist of the Photo electric sensor, the incoming AC power line sense, the comparator / controller report circuitry and the Dimmer Control Circuitry all resident in the CIA Unit. The system shall sense the background ambient light level (range 0 to 5 Volts) and provide sense levels to the controller via the 8 Sense Lines. The AC Line shall be sensed between 75 to 150 VAC and reported back to the controller via sense lines. The Dimmer Control Circuit shall read the three Dim Level select lines from the controller and control the Triac Turn On Point.
- 8.6.2.1.2 Background ambient light levels for pixel dimming and background light sensing shall be adjusted and pre-set at the factory prior to delivery to the STATE. Potentiometer settings shall match each light level setting designated by number from level 1 to 7, where level 1 is lowest light level and level 7 is highest (brightest) light level.

8.6.2.2 FEATURE REQUIREMENTS

- 8.6.2.2.1 Background Light Sensing Circuitry
- 8.6.2.2.1.1 A commercially available photo-electric sensor in a EEI-NEMA type twist lock receptacle or equal, metal enclosure shall be installed on the Controller Cabinet, where the sensor shall not be affected by artificial lighting.
- 8.6.2.2.1.2 The photo-electric sensor shall be a EG and G VACTEC Type VT521H or equal.
- 8.6.2.2.1.3 The photo-electric sensor shall be capable of being continually exposed to direct sunlight without impairment of its performance.
- 8.6.2.2.1.4 The watertight metal enclosure shall have a 1.00 square inch (2.54cm²) glass window area to assure full exposure of photocell to ambient light.
- 8.6.2.2.1.5 The Photo- electric sensor output lines shall be routed to Cabinet Terminal Block TB1 and via C10 to the CIA. The outputs shall be electrical compatible with the CIA Input circuitry.
- 8.6.2.3 Pixel Dimming Circuitry CIA
- 8.6.2.3.1 The pixel dimming circuit shall allow the AC power to be switched at any point from 10 degrees to 120 degrees and from 190 degrees to 300 degrees for each pixel intensity.
- 8.6.2.3.2 Each pixel intensity sine wave switch point shall vary 5 degrees (maximum) when the dimming circuit is exposed to temperature changes ranging from $+25^{\circ}$ C to $+65^{\circ}$ C and from $+25^{\circ}$ C to -30° C.

- 8.6.2.3.3 Each pixel intensity shall be adjusted by a discrete wirewound 10-turn potentiometer. Each potentiometer shall use 90% (maximum) of its adjustment range throughout the sine wave switching range specified.
- 8.6.2.3.4 The pixel dimming circuit shall read 3 Dimmer Level inputs (read in binary, Line 1 least significant), from the controller to select the 8 set conditions of dim control for the CMS (Selection "0" shall allow no Dim Control output or BLANK Sign).
- 8.6.2.3.5 The pixel dimming circuit shall have electrical devices installed to minimize 60 Hz noise on the AC load lines.
- 8.6.2.3.5.1 Each noise eliminator shall cause the slope of the switch-on across the pixels to rise at between 8V per degree and 20V per degree across the AC voltage sine wave.
- 8.6.2.3.5.2 Each noise eliminator shall eliminate overshoot across the AC voltage sine wave when the pixel load voltage reaches its maximum.
- 8.6.2.4 The DIM Control Line shall control all 60 PDM Triac Turn On Points. The DIM Control Circuitry shall relay back to the Controller a Phase Fire Pulse of 200 us nominal indicating Triac Turn On pulse. The DIM Control shall, also, provide Soft Start Feature. Soft Start shall use the DIM Control line to reduce in rush current at pixel turn on or message change. Soft Start shall have a six AC cycle minimum step between 160 deg to 10 deg and 340 deg to 190 deg.

SECTION 7

GENERAL CMS PLAN DETAILS

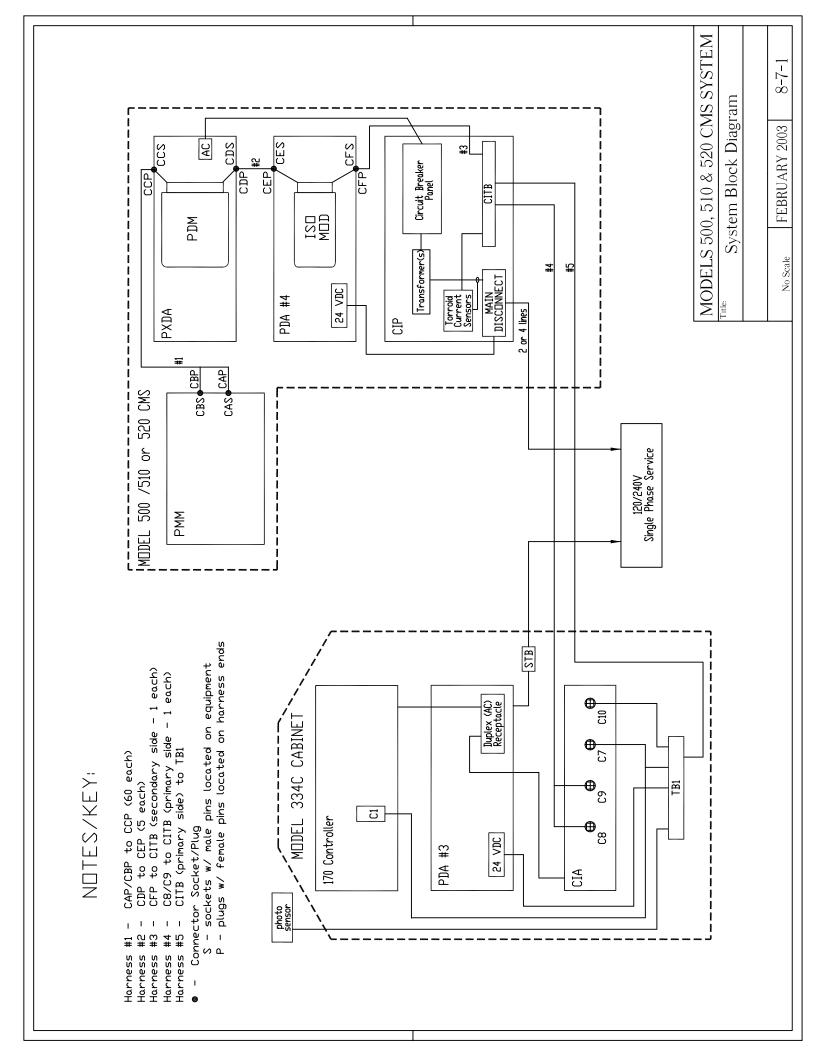
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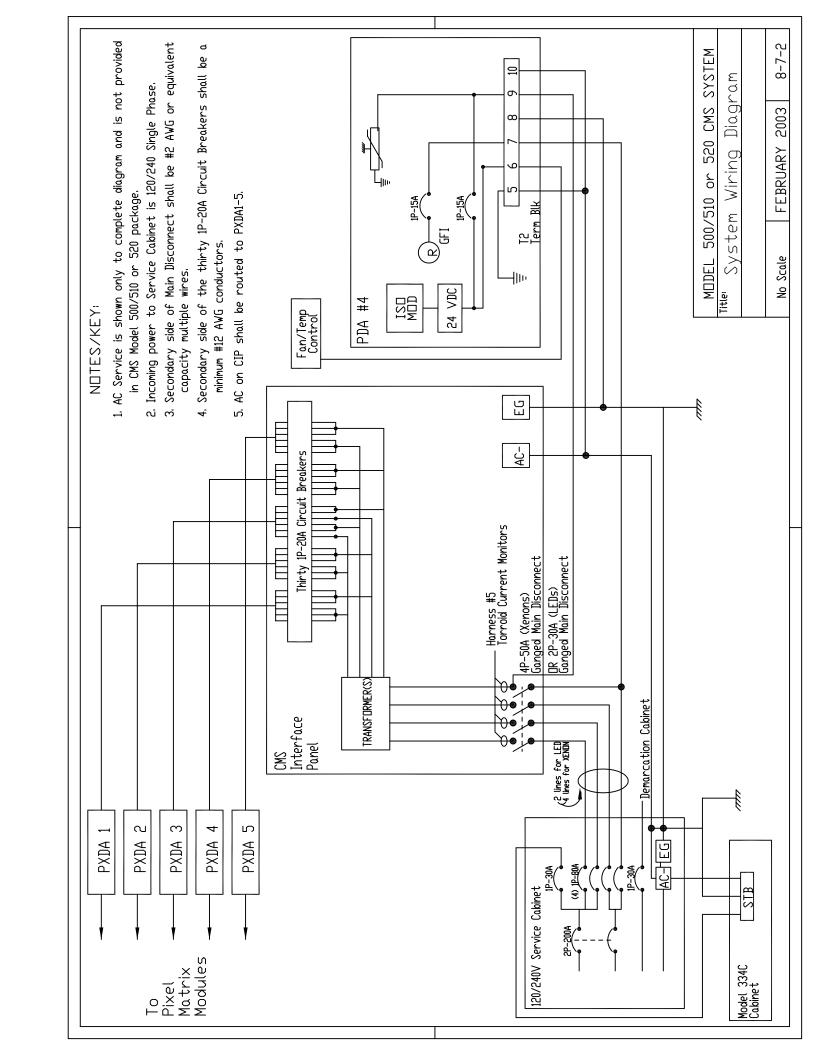
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GENERAL CMS PLAN DETAILS

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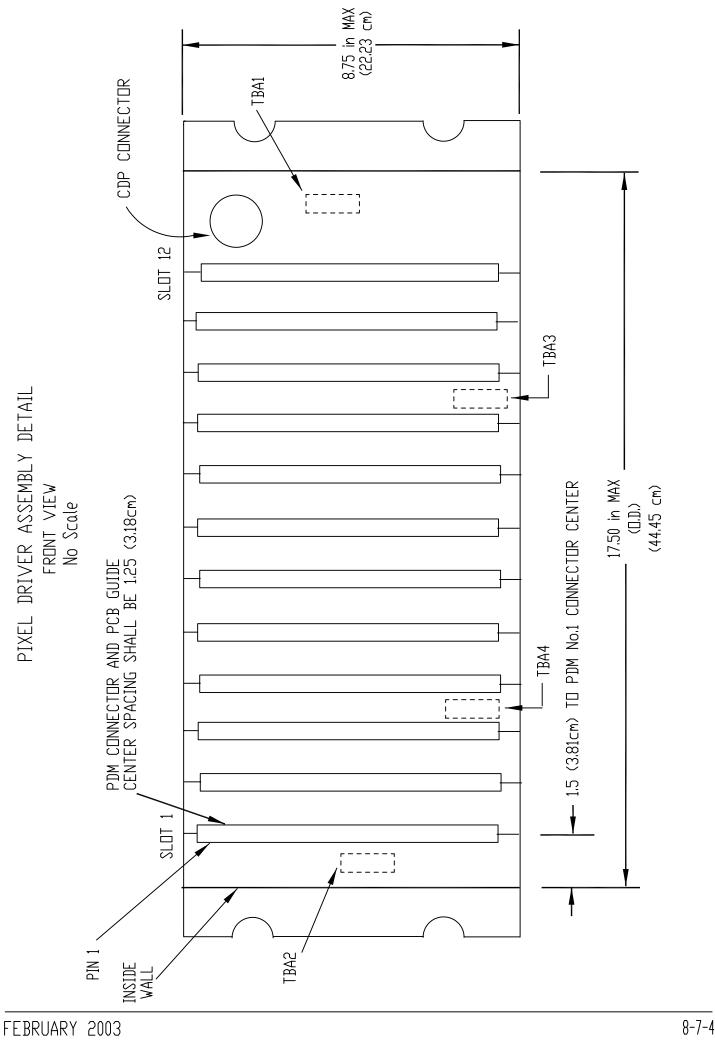
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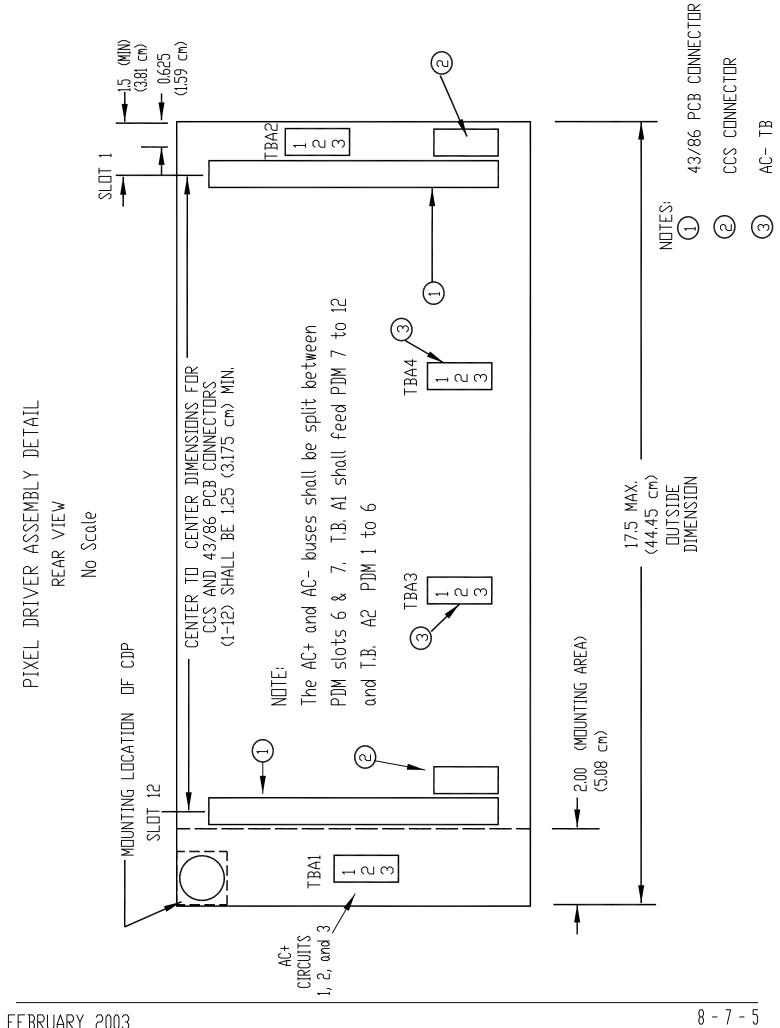




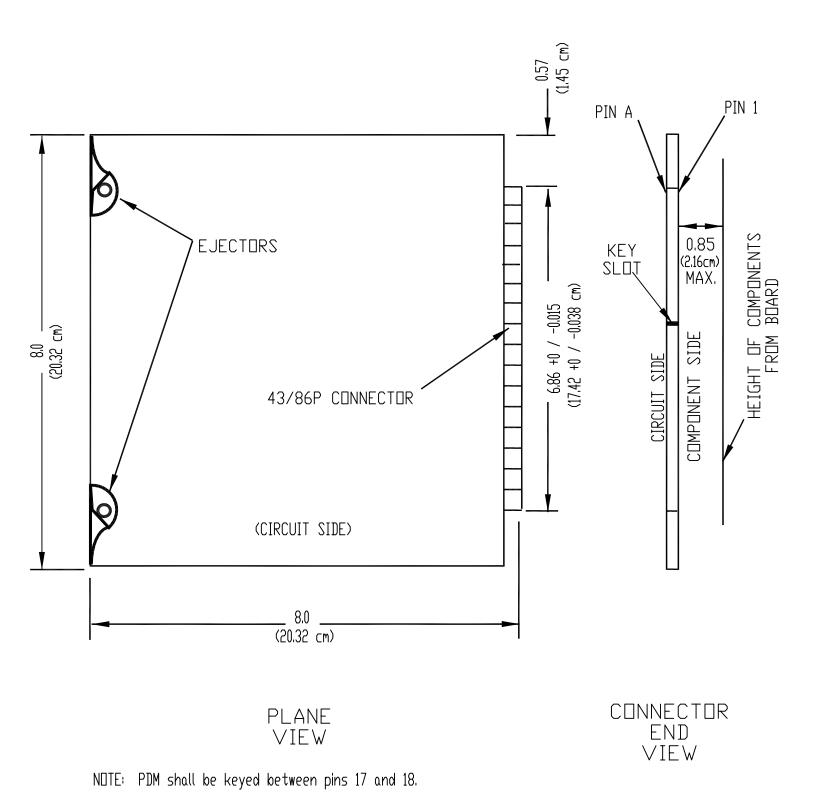
(Neutral) (Line) Fused AC+ OUTPUT CIRCUIT Triac PIXEL DRIVER MODULE CIRCUIT BLOCK DIAGRAM No Scale Opto-Isolated Triac Drivers ISOLATION AND AC SWITCHING Comparators Shift Registers Parallel Latches DATA & Control 12V Logic INPUT CIRCUIT 24 VDC

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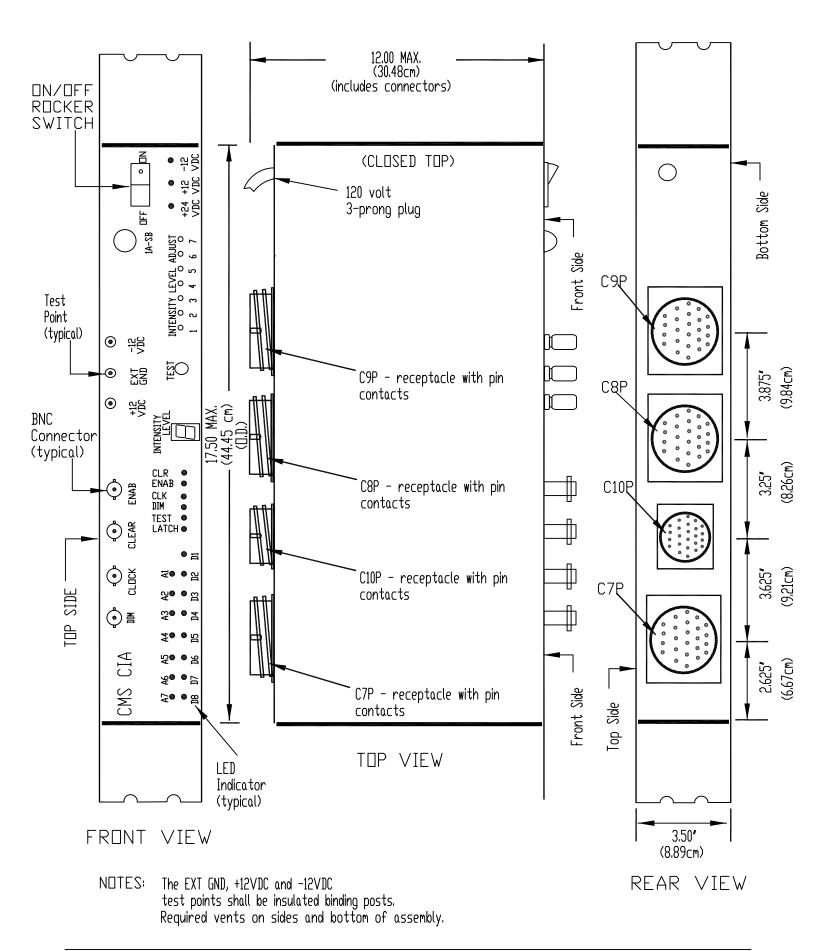


PIXEL DRIVER MODULE No Scale

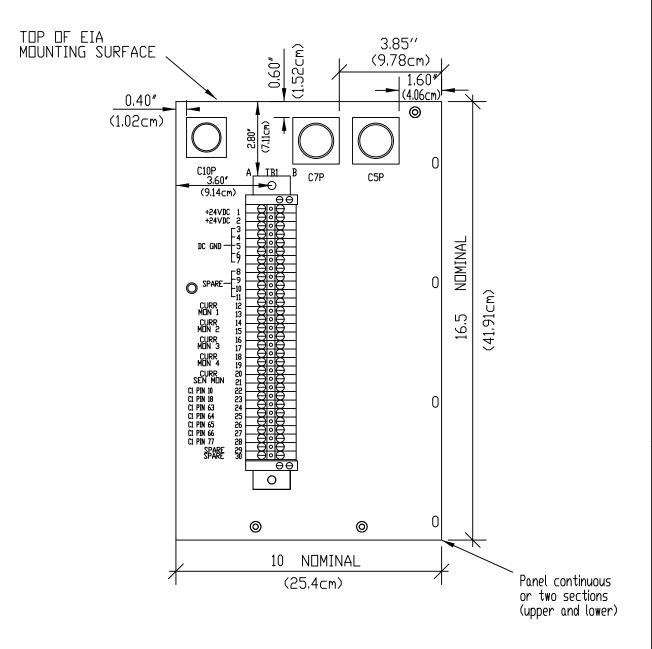


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CONTROLLER ISOLATION ASSEMBLY No Scale



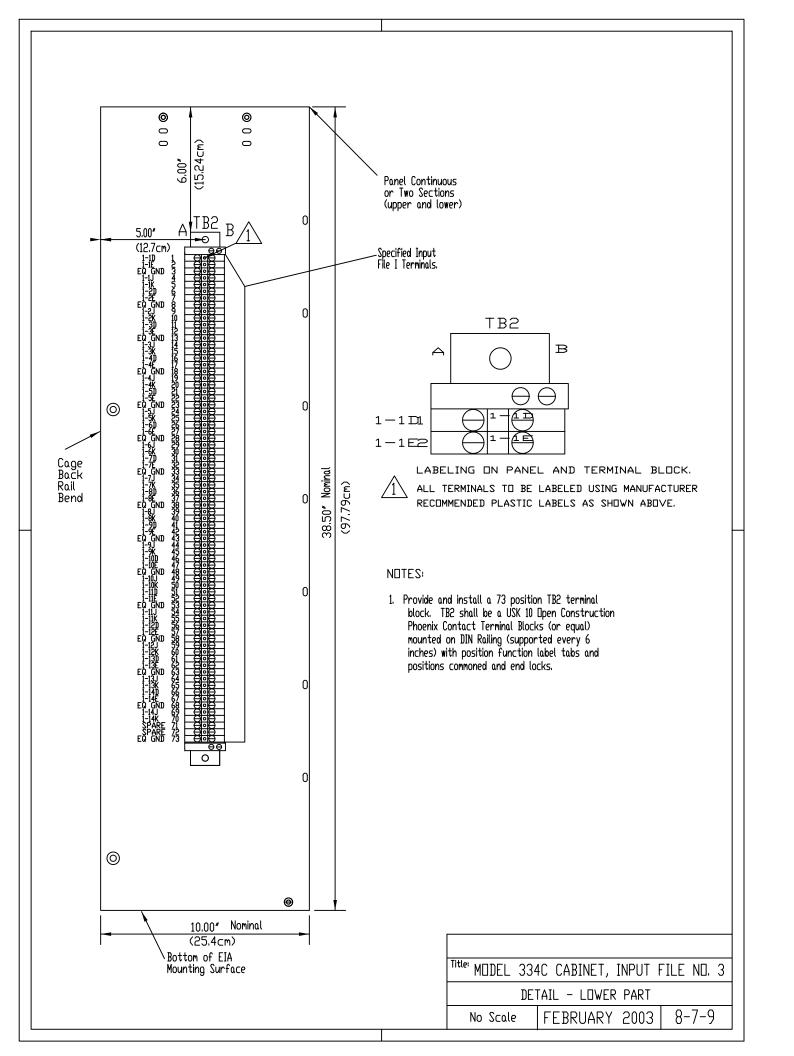
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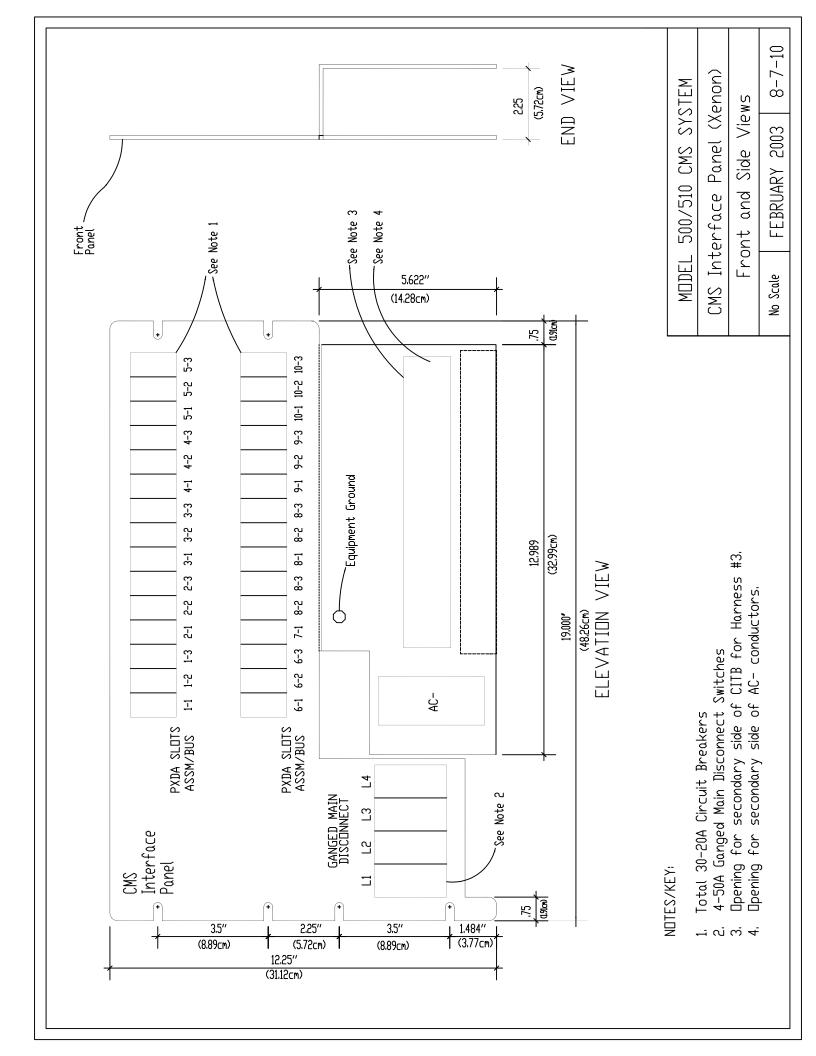


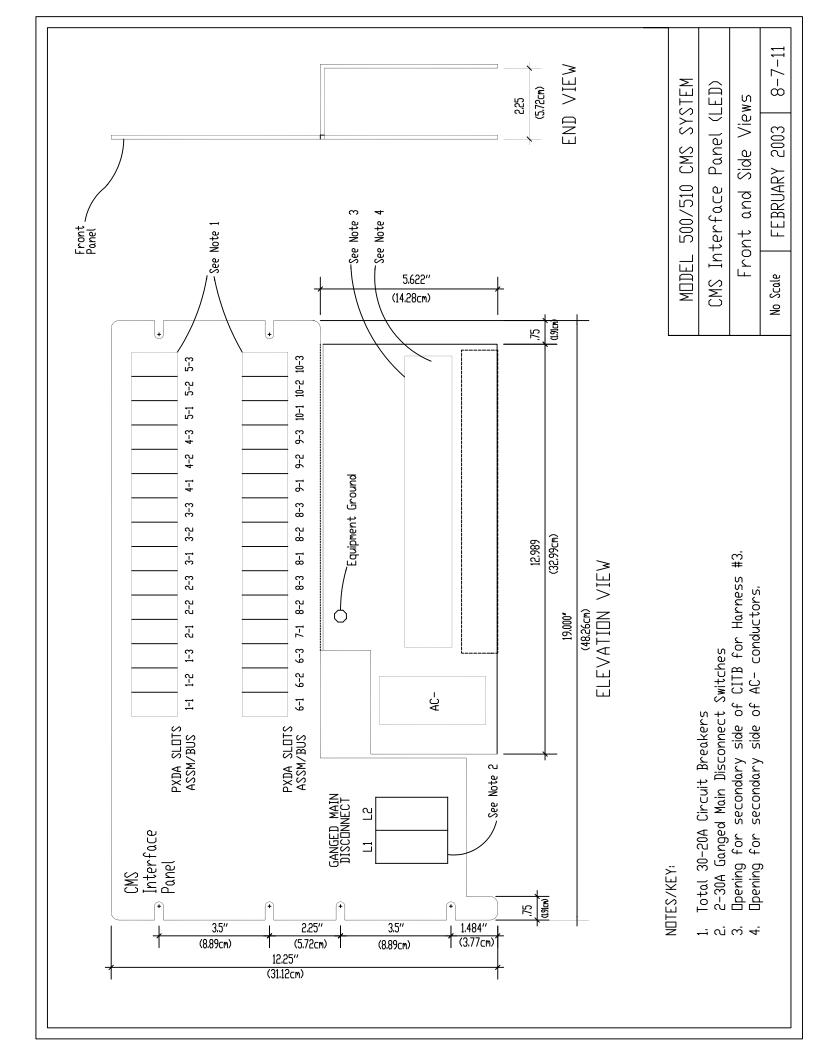
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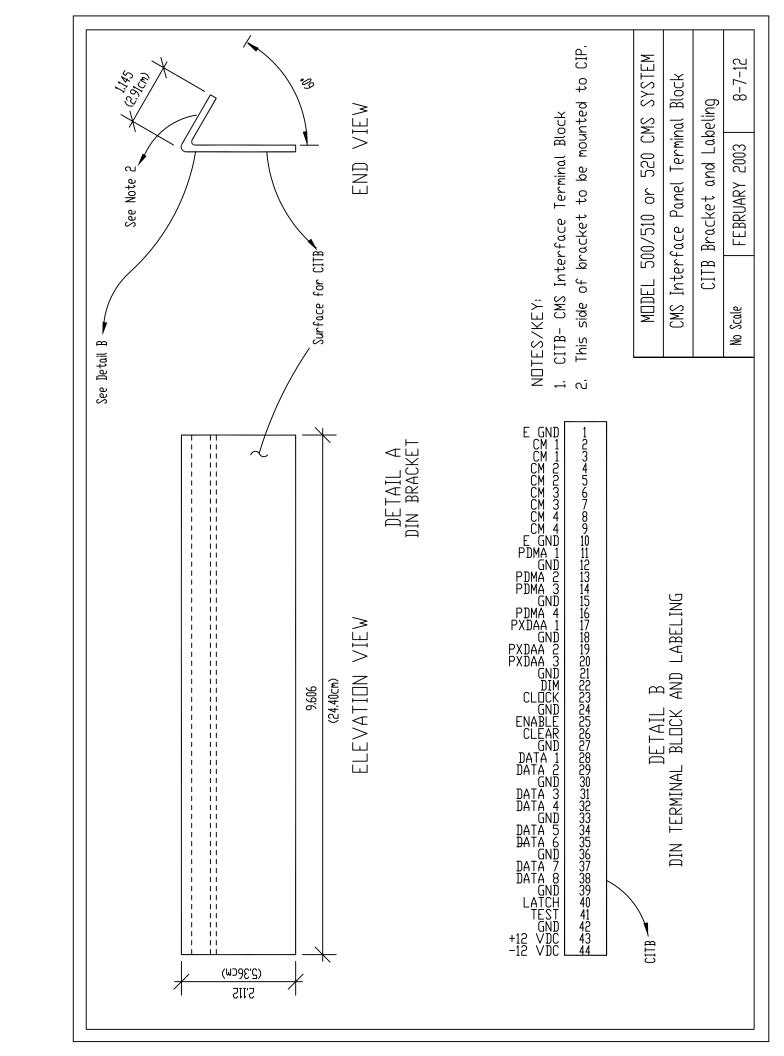
- 1. Provide and install a 30 position TB1 Terminal Block and three circular connectors, C5P, C7P and C10P (no contracts). TB1 shall be a USK 10 Open Construction Phoenix Contact Terminal Block (or equal) mounted on DIN Railing (supported every 6 inches (15.24cm)) with position function label tabs and positions commoned and end locks.
- 2. A 4 foot length 'CMS' Harness of 14 #20 (or larger) Conductors shall be furnished and installed in the cabinet. One end of the harness shall be the C10S Connector resting in C10P (mounted on the Input Panel #3) when not in use. The other end shall be stripped (according to manufacturers requirements) and connected to the 'B Side' of TB1. The conductor bundle shall have external protection.

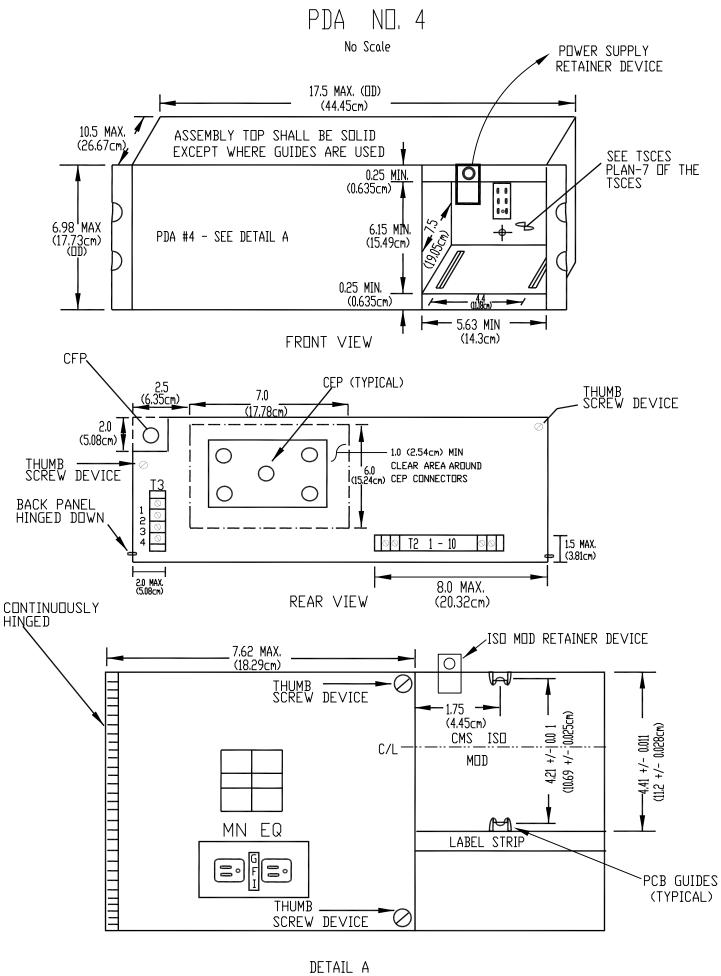
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DETAIL - UPPER PART					
No Scale	FEBRUARY 2003	8-7-8			



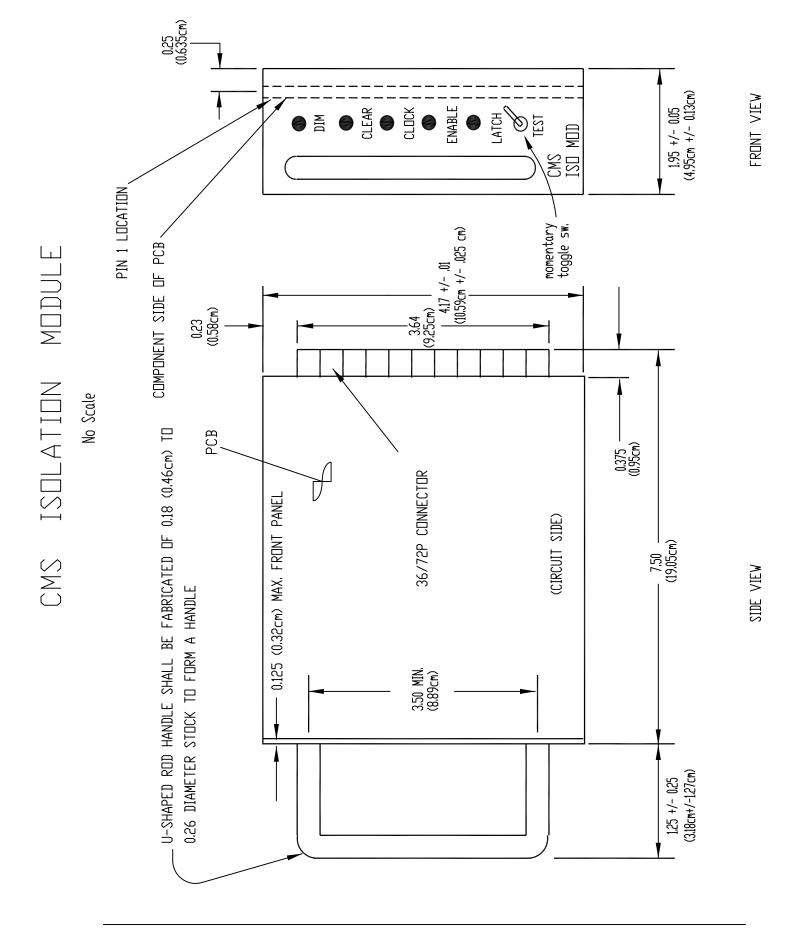


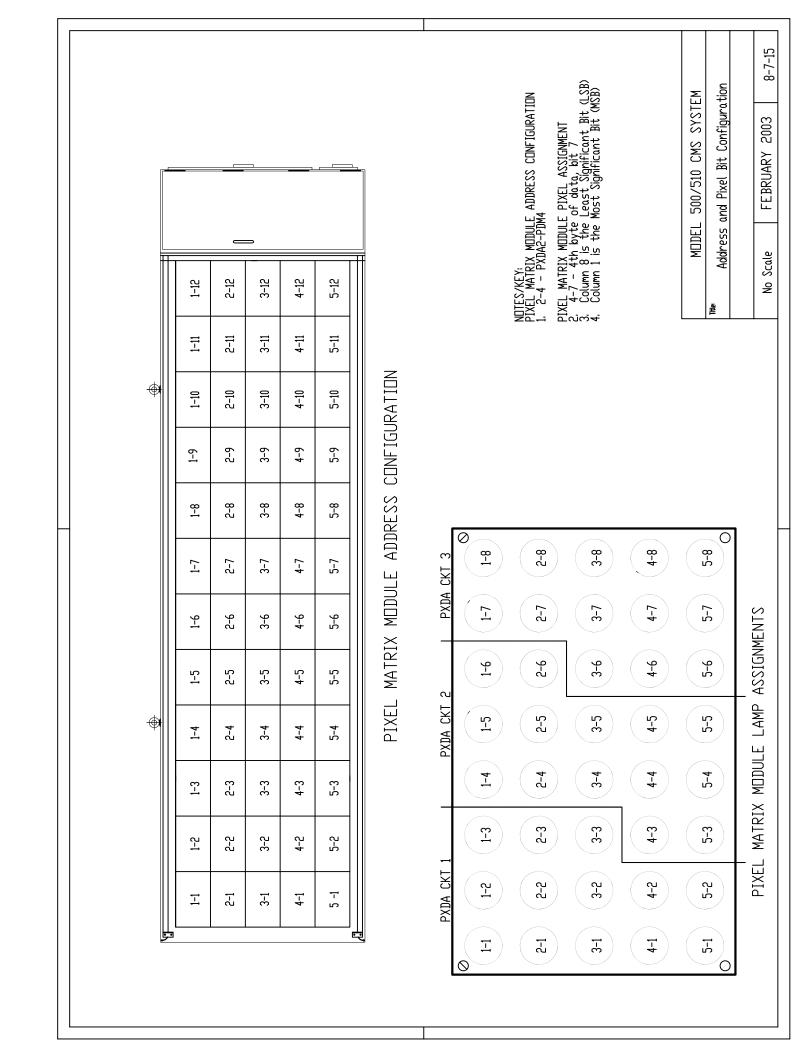






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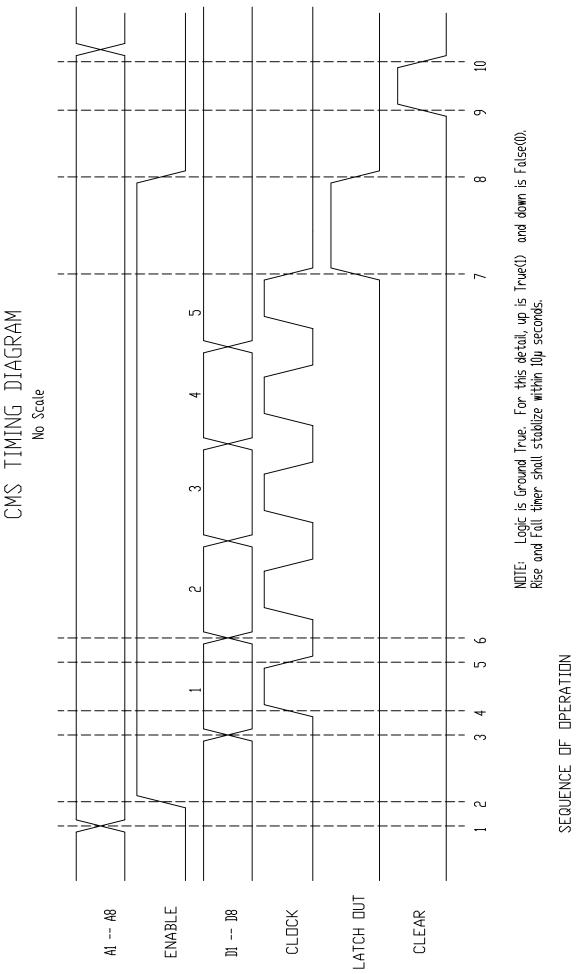




CMS DEVICE ADDRESS ASSIGNMENT

Device Designation	PX	DA Addre	ess (3 bits)	LDM S	lot Ad	dress (4	bits)
	7	6	5	4	3	2	1
PXDA1 - PDM 1	0	0	1	0	0	0	1
PXDA1 - PDM 2	0	0	1	0	0	1	0
PXDA1 - PDM 3	0	0	1	0	0	1	1
PXDA1 - PDM 4	0	0	1	0	1	0	0
PXDA1 - PDM 5	0	0	1	0	1	0	1
PXDA1 - PDM 6	0	0	1	0	1	1	0
PXDA1 - PDM 7	0	0	1	0	1	1	1
PXDA1 - PDM 8	0	0	1	1	0	0	0
PXDA1 - PDM 9	0	0	1	1	0	0	1
PXDA1 - PDM 10	0	0	1	1	0	1	0
PXDA1 - PDM 11	0	0	1	1	0	1	1
PXDA1 - PDM 12	0	0	1	1	1	0	0
PXDA2 - PDM 1	0	1	0	0	0	0	1
PXDA2 - PDM 2	0	1	0	0	0	1	0
PXDA2 - PDM 3	0	1	0	0	0	1	1
PXDA2 - PDM 4	0	1	0	0	1	0	0
PXDA2 - PDM 5	0	1	0	0	1	0	1
PXDA2 - PDM 6	0	1	0	0	1	1	0
PXDA2 - PDM 7	0	1	0	0	1	1	1
PXDA2 - PDM 8	0	1	0	1	0	0	0
PXDA2 - PDM 9	0	1	0	1	0	0	1
PXDA2 - PDM 10	0	1	0	1	0	1	0
PXDA2 - PDM 11	0	1	0	1	0	1	1
PXDA2 - PDM 12	0	1	0	1	1	0	0
PXDA3 - PDM 1	0	1	1	0	0	0	1
PXDA3 - PDM 2	0	1	1	0	0	1	0
PXDA3 - PDM 3	0	1	1	0	0	1	1
PXDA3 - PDM 4	0	1	1	0	1	0	0
PXDA3 - PDM 5	0	1	1	0	1	0	1
PXDA3 - PDM 6	0	1	1	0	1	1	0
PXDA3 - PDM 7	0	1	1	0	1	1	1
PXDA3 - PDM 8	0	1	1	1	0	0	0
PXDA3 - PDM 9	0	1	1	1	0	0	1
PXDA3 - PDM 10	0	1	1	1	0	1	0
PXDA3 - PDM 11	0	1	1	1	0	1	1
PXDA3 - PDM 12	0	1	1	1	1	0	0
PXDA4 - PDM 1	1	0	0	0	0	0	1
PXDA4 - PDM 2	1	0	0	0	0	1	0
PXDA4 PDM 3	1	0	0	0	0	1	1
PXDA4 PDM 4	1	0	0	0	1	0	0
PXDA4 PDM 5	1	0	0	0	1	0	1
PXDA4 PDM 6	1 1	0	0	0	1	1	0
PXDA4 PDM 7	1	0	0	0	1	1	1
PXDA4 - PDM 8 PXDA4 - PDM 9	1	0	0	1 1	0	0	0 1
PXDA4 - PDM 9 PXDA4 - PDM 10	1	0	0	1	0	1	0
PXDA4 - PDM 10 PXDA4 - PDM 11	1	0	0	1	0	1	1
PXDA4 - PDM 12	1	0	0	1	1	0	0
PXDA5 - PDM 1	1	0	1	0	0	0	1
PXDA5 - PDM 2	1	0	1	0	0	1	0
PXDA5 - PDM 3	1	0	1	0	0	1	1
PXDA5 - PDM 4	1	0	1	0	1	0	0
PXDA5 - PDM 5	1	0	1	0	1	0	1
PXDA5 - PDM 6	1	0	1	0	1	1	0
PXDA5 - PDM 7	1	0	1	0	1	1	1
PXDA5 - PDM 8	1	0	1	1	0	0	0
PXDA5 - PDM 9	1	0	1	1	0	0	1
PXDA5 - PDM 10	1	0	1	1	0	1	0
PXDA5 - PDM 10	1	0	1	1	0	1	1
PXDA5 - PDM 12	1	0	1	1	1	0	0
	*	Ü	-	1	1	V	J

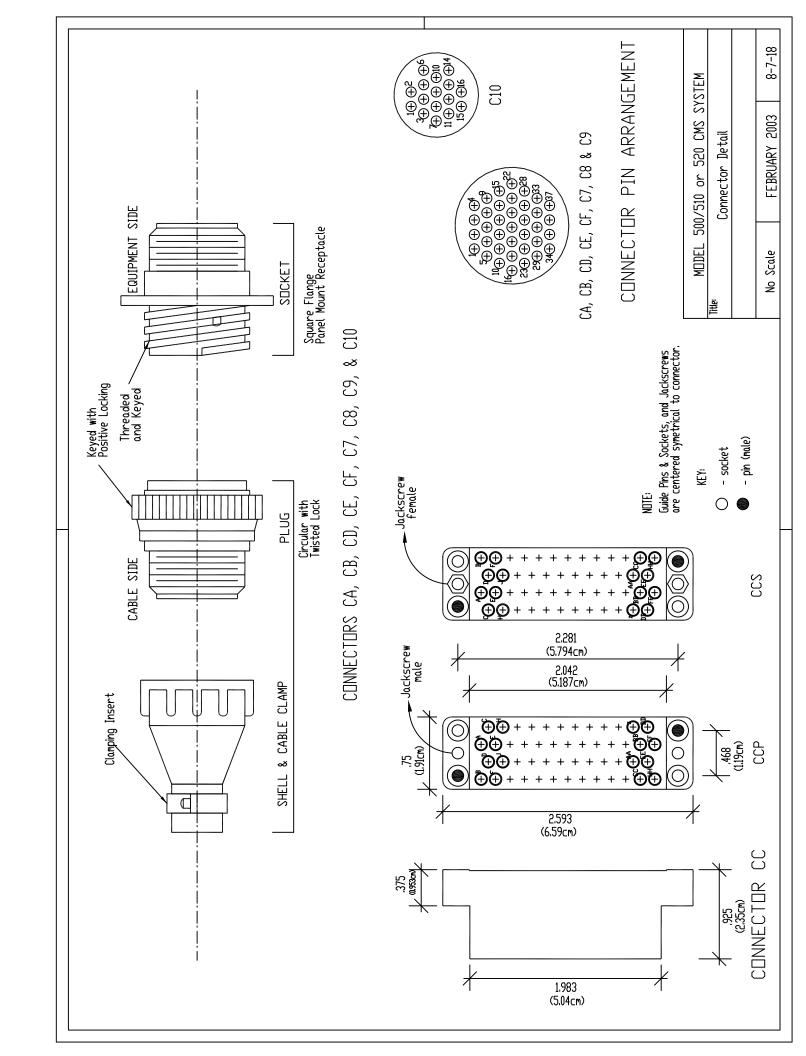
FEBRUARY 2003

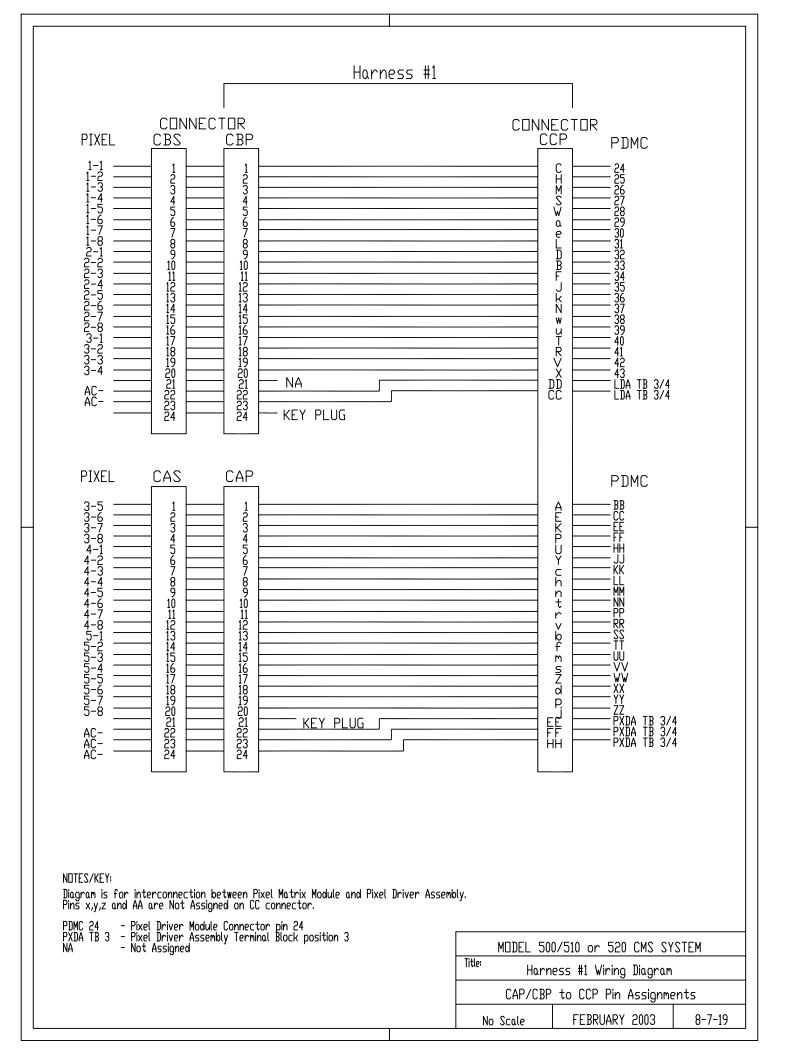


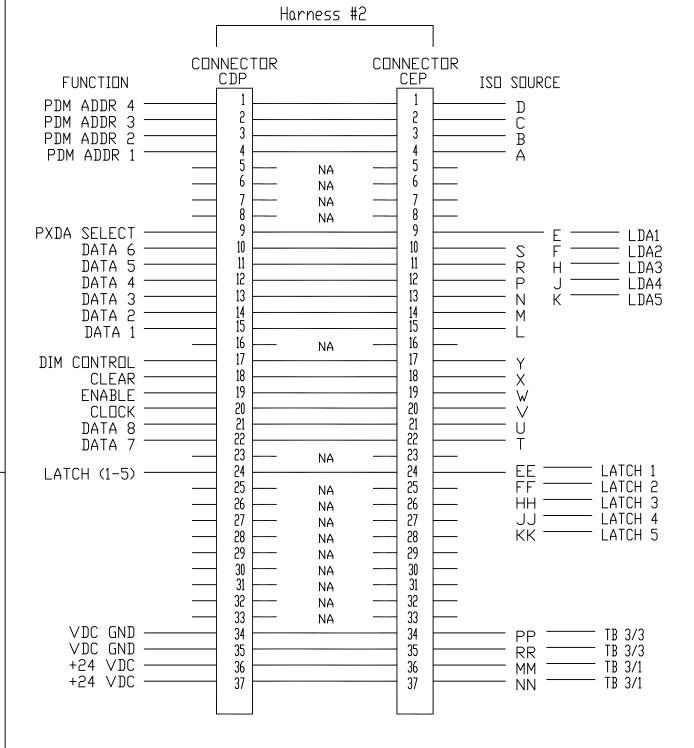
- Place Address on Address Lines.
 - Set the Enable Line to True,
- Begin Data transfer loop, place Data on Data Lines. First word/Fifth row onto Fifth word/First row of Pixels. Set the Clock Line to True. The leading edge will shift data in shift register. Set the Clock Line to False. Place the next Data on the Data Lines and loop until all 5 bytes of data are transferred.

- The trailing edge of the Clock Line will cause the Latch Line to go True. Set the Enable Line to False. The Latch Line will go False. Set the Clear Line to True. This will turn off Pixel Load Triacs and transfer data from shift registers to output latches. Set the Clear Line to False. This enables Pixel Load Triacs with new message.

8-7-17 FEBRUARY 2003







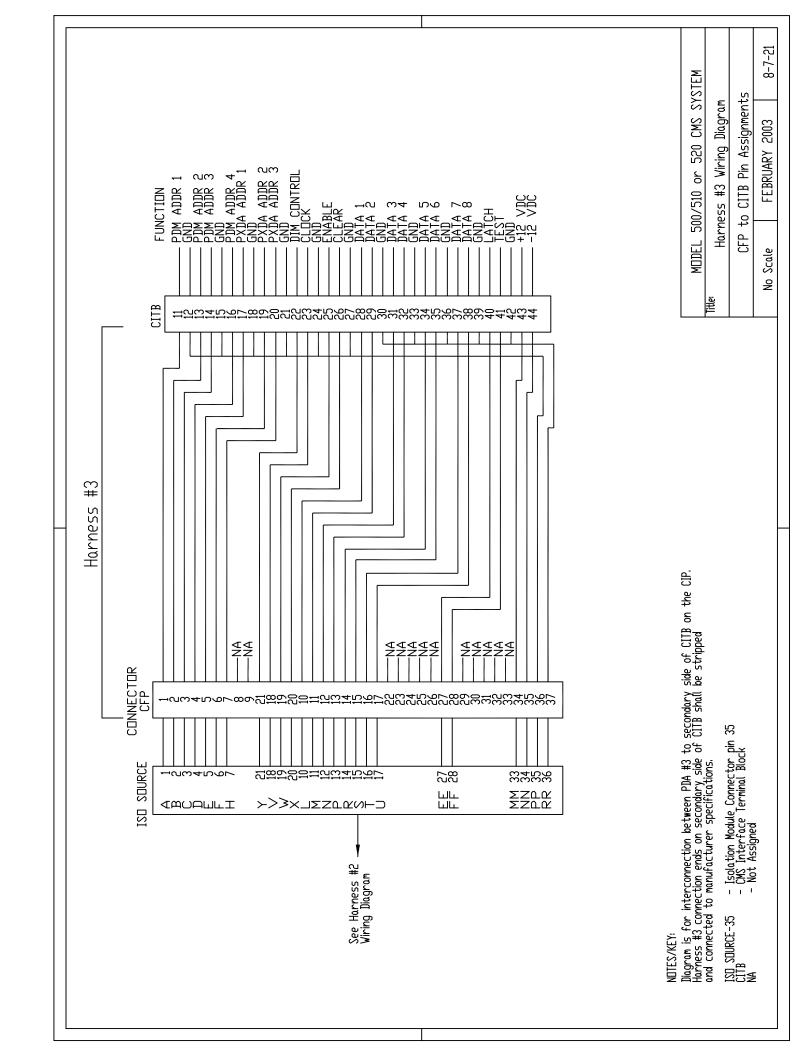
NOTES/KEY:

Diagram is for interconnection between Pixel Driver Assembly and PDA #3 ISD-54, LATCH 1 pertains to PXDA 1; ISD-56, LATCH 2 pertains to PXDA 2, etc.

ISD SDURCE-60 PXDA TB 3 NA

- Isolation Module Connector pin 60 - Pixel Driver Assembly Terminal Block position 3 - Not Assigned

	MODEL	50	0/510 or 520 CMS SY	'STEM	
Title: Harness #2 Wiring Diagram					
CDP to CEP Pin Assignments					
No Scale FEBRUARY 201			FEBRUARY 2003	8-7-20	



CMS HARNESS NO. 4 AND 5 WIRING LISTS

CONTROLLER INTERFACE PANEL TERMINAL BLOCK (CITB)

CAB/CIA CONNECTORS

HARNESS NO. 4

TERMINAL BLOCK	POSITION	FUNCTION	CONNECTOR PIN
CITB	11	PDM Addr 1	C8 1
CITB	13	PDM Addr 2	C83
CITB	14	PDM Addr 3	C85
CITB	16	PDM Addr 4	C8 7
CITB	17	PXDA Addr 1	C89
CITB	19	PXDA Addr 2	C8 11
CITB	20	PXDA Addr 3	C8 13
CITB	22	DIM CONTROL	C9 7
CITB	23	CLOCK	C9 1
CITB	25	ENABLE	C9 3
CITB		CLEAR	C9 5
CITB12,1	5,18,21,24,27	EXT VDC GND	C8*
CITB	28	DATA 1	C8 19
CITB	29	DATA 2	C8 21
CITB	31	DATA 3	C823
CITB	32	DATA 4	C825
CITB	34	DATA 5	C827
CITB	35	DATA 6	C829
CITB	37	DATA 7	C8 31
CITB	38	DATA 8	C8 33
CITB	40	LATCH LINE	C9 13
CITB	41	TEST	C9 15
CITB	43	EXT +12 VDC	C934
CITB		EXT -12 VDC	C935
CITB3	0,33,36,39,42	EXT VDC GND	C8 & 9*

HARNESS NO. 5

TERMINAL BLOCK	POSITION	FUNCTION	CONNECTOR-PIN
CITB	1 & 10	EQUIP. GROUND	
CITB	2 & 3	CURRENT MONITOR 1	TB1A-12&13/C10-1&2
CITB	4 & 5	CURRENT MONITOR 2	TB1A-14&15/C10-3&4
CITB	6 & 7	CURRENT MONITOR 3	TB1A-16&17/C10-5&6
CITB	8 & 9	CURRENT MONITOR 4	TB1A-18&19/C10-7&8

^{*} VDC LOGIC GND WIRES, (refer to page 8-7-28).

DEFINITIONS: PDM Addr 1 - Lamp Driver Module Address Line 1.

PXDA Addr 1 - Lamp Driver Assembly Address Line 1.

CMS HARNESS #4 COLOR CODED WIRING LIST

FUNCTION	CIP/CITB POSITION	COLOR PAIRS (gnd w/logic)	CONNECTOR-PINS (logic, gnd)
PDM Addr 1	11	BLACK W/RED	C8-1,2
PDM Addr 2	13	BLACK W/WHITE	C8-3,4
PDM Addr 3	14	BLACK W/GREEN	C8-5,6
PDM Addr 4	16	BLACK W/BLUE	C8-7,8
PXDA Addr 1	17	BLACK W/YELLOW	C8-9,10
PXDA Addr 2	19	BLACK W/BROWN	C8-11,12
PXDA Addr 3	20	BLACK W/ORANGE	C8-13,14
DIM CONTROL	22	RED W/WHITE	C9-7,8
CLOCK	23	RED W GREEN	C9-1,2
ENABLE	25	RED W/BLUE	C9-3,4
CLEAR	26	RED W/YELLOW	C9-5,6
DATA 1	28	RED W/BROWN	C8-19,20
DATA 2	29	RED W/ORANGE	C8-21,22
DATA 3	31	GREEN W/WHITE	C8-23,24
DATA 4	32	GREEN W/BLUE	C8-25,26
DATA 5	34	GREEN W/YELLOW	C8-27,28
DATA 6	35	GREEN W/BROWN	C8-29,30
DATA 7	37	GREEN W/ORANGE	C8-31,32
DATA 8	38	WHITE W/BLUE	C8-33,34
LATCH LINE	40	WHITE W/YELLOW	C9-13,14
TEST	41	WHITE W/BROWN	C9-15,16
EXT +12 VDC	43	WHITE W/ORANGE	C9-34,36
EXT -12 VDC	44	BLUE W/YELLOW	C9-35,37

^{*}CIP/CITB VDC LOGIC GND POSITIONS: 12, 15, 18, 21, 24, 27, 30, 33, 36, 39, 42

CMS HARNESS # 5 COLOR CODED WIRING LIST

FUNCTION	CIP/CITB POSITION	COLOR PAIRS	CONNECTOR (TB1A-C10)
CURR. MONITOR 1	2 & 3	BLACK W/RED	12&13-1&2
CURR. MONITOR 2	4 & 5	BLACK W/WHITE	14&15-3&4
CURR. MONITOR 3	6 & 7	BLACK W/GREEN	16&17-5&6
CURR. MONITOR 4	8 & 9	BLACK W/BLUE	18&19-7&8

DEFINITIONS:

TB1A-Terminal Block 1A in 334C cabinet.

TB1A-C10: terminal block 1A position 1&2 connected to C10 connector pins 1&2.

MODEL 334C CONTROLLER CABINET HARNESS NO. 5 AND TB1 WIRING LIST

CABINET TB1A TERMINAL BLOCK

POSITION	SIDE A	SIDE B	C10 CONNECTOR
1 2 3 - 7	+24 VDC (PDA 3) +24 VDC (POS. 1) DC GND (PDA 3)	I/O FILES CAB. HARNESS #5 C1 PINS 1 & 104, I/O FILES &	PINS 13 & 14
8 - 11	NA	CAB. HARNESS #5 NA	PINS 15 & 16
12 & 13	CURR. MONITOR 1*	CAB. HARNESS #5	PINS 1 & 2
14 & 15	CURR. MONITOR 2*	CAB. HARNESS #5	PINS 3 & 4
16 & 17	CURR. MONITOR 3*	CAB. HARNESS #5	PINS 5 & 6
18 & 19	CURR. MONITOR 4*	CAB. HARNESS #5	PINS 7 & 8
20 & 21	PHOTO SEN. MON.	CAB. HARNESS #5	PINS 9 & 10
22	C1 PIN 10	CIA CONTROL 4	PIN 11
23	C1 PIN 18	NA	
24	C1 PIN 63	NA	
25	C1 PIN 64	NA	
26	C1 PIN 65	POL. CONTROL SWITCH	
27	C1 PIN 66	POL. LIGHTS SWITCH	
28	C1 PIN 76	NA	
29	C1 PIN 77	NA	
30	NA	NA	

^{*} Refers to CMS HARNESS #5.

NOTES: All C1 PINS refer to C1 HARNESS #2.

MODEL 334C CABINET - C1 HARNESS NO. 2 WIRING LIST (Interconnection between CABINET and CONTROLLER)

PIN	SOURCE	DESTINATION	FUNCTION	PIN	SOURCE	DESTINATION	FUNCTION
1	DC GND	TB1/3-7	DC GND BUS	53	I2-7	IFI-11F	RATE CODE 2
2	O1-1	C6-1	SWPK 1-RED	54	I2-8	IFI-11W	RATE CODE 1
3	O1-2	C6-2	SWPK 1-GRN	55	I3-1	IFI-4F	MAIN 9
4	O1-3	C6-3	SWPK 2-RED	56	I3-2	IFI-4W	MAIN 10
5	O1-4	C6-4	SWPK 2-YEL	57	I3-3	IFI-6F	MAIN 3
6	O1-5	C6-5	SWPK 2-GRN	58	I3-4	IFI-6W	MAIN 4
7	O1-6	C6-6	SWPK 3-RED	59	I3-5	IFI-7F	MAIN 5
8	O1-7	C6-7	SWPK 3-YEL	60	I3-6	IFI-7W	MAIN 6
9	O1-8	C6-8	SWPK 3-GRN	61	I3-7	IFI-8F	MAIN 11
10	O2-1	TB1/22	CIA CONTROL 4	62	I3-8	IFI-8W	MAIN 12
11	O2-2	C7-25	CMS CLOCK	63	I4-5	TB1/24	MAIN 17
12	O2-3	C7-26	CMS ENABLE	64	I4-6	TB1/25	MAIN 18
13	O2-4	C7-27	CMS CLEAR	65	I4-7	TB1/26	POL CONT'L SW
14	DC GND	IFI-15-4	INPUT DC GND	66	I4-8	TB1/27	POL LIGHTS SW
15	O2-5	C7-28	CMS DIM LEVEL 1	67	I5-1	C7-16	CIA SENSE 1
16	O2-6	C7-29	CMS DIM LEVEL 2	68	I5-2	C7-31	CIA SENSE 2
17	O2-7	C7-30	CMS DIM LEVEL 3	69	I5-3	C7-32	CIA SENSE 3
18	O2-8	TB1/23		70	I5-4	C7-33	CIA SENSE 4
19	O3-1	C7-9	CMS ADDRESS 1	71	I5-5	C7-34	CIA SENSE 5
20	O3-2	C7-10	CMS ADDRESS 2	72	I5-6	C7-35	CIA SENSE 6
21	O3-3	C7-11	CMS ADDRESS 3	73	I5-7	C7-36	CIA SENSE 7
22	O3-4	C7-12	CMS ADDRESS 4	74	I5-8	C7-37	CIA SENSE 8
23	O3-5	C7-13	CMS ADDRESS 5	75	I6-1	C7-5	CMS LATCH
24	O3-6	C7-14	CMS ADDRESS 6	76	I6-2	C7-6	PHASE FIRE
25	O3-7	C7-15	CMS ADDRESS 7	77	I6-3	TB1/29	MAIN 19
26	O3-8	C7-4	CIA CONTROL 5	78	I6-4	C7-8	CMS TEST REQ.
27	O4-1	C7-17	CMS DATA 1	79	I6-5	IFI-10F	MAIN 15
28	O4-2	C7-18	CMS DATA 2	80	I6-6	IFI-9W	MAIN 14
29	O4-3	C7-19	CMS DATA 3	81	I6-7	IFI-9F	MAIN 13
30	O4-4	C7-20	CMS DATA 4	82	I6-8	IFI-10W	MAIN 16
31	O4-5	C7-21	CMS DATA 5	83	O6-1	C5-1	SWPK 14-RED
32	O4-6	C7-22	CMS DATA 6	84	O6-2	C5-2	SWPK 14-GRN
33	O4-7	C7-23	CMS DATA 7	85	O6-3	C5-3	SWPK 13-RED
34	O4-8	C7-24	CMS DATA 8	86	O6-4	C5-4	SWPK 13-YEL
35	O5-1	C7-1	CIA CONTROL 1	87	O6-5	C5-5	SWPK 13-GRN
36	O5-2	C7-2	CIA CONTROL 2	88	O6-6	C5-6	SWPK 12-RED
37	O5-3	C6-9	SWPK 1-YEL	89	O6-7	C5-7	SWPK 12-YEL
38	O5-4	C7-3	CIA CONTROL 3	90	O6-8	C5-8	SWPK 12-GRN
39	I1-1	IFI-1W	PASSAGE 1	91	O7-1	C5-9	SWPK 11-RED
40	I1-2	IFI-12W	PASSAGE 2	92	DC GND	TB1/3-7	
41	I1-3	IFI-12F	DEMAND 2	93	O7-2	C5-10	SWPK 11-GRN
42	I1-4	IFI-13W	OFF RAMP 2	94	O7-3	C5-11	SWPK 10-RED
43	I1-5	IFI-13F	QUE 2	95	O7-4	C5-12	SWPK 10-YEL
44	I1-6	IFI-14W	PASSAGE 3	96	O7-5	C4-13	SWPK 10-GRN
45	I1-7	IFI-14F	DEMAND 3	97	O7-6	C5-14	SWPK 9-RED
46	I1-8	IFI-1F	DEMAND 1	98	O7-7	C5-15	SWPK 9-YEL
47	I2-1	IFI-2W	OFF RAMP 1	99	O7-8	C5-16	SWPK 9-GRN
48	I2-2	IFI-3W	MAIN 7	100	O5-5	C5-17	SWPK 14-YEL
49	I2-3	IFI-3F	MAIN 8	101	O5-6	C5-18	SWPK 11-YEL
50	I2-4	IFI-2F	QUE 1	102	O5-7	IFI-15-3	DET. RESET
51	I2-5	IFI-5F	MAIN 1	103	O5-8	C6-10	WDT
52	I2-6	IFI-5W	MAIN 2	104	DC GND	TB1/3-7	DC GND BUS
							2 22.2 200

NOTES: C7S CONNECTOR PIN 7 is not assigned. TB1/26 - TERMINAL BLOCK 1 POSITION 26.

PDA NO.4 CMS ISO MOD and CFP CONNECTOR ASSIGNMENTS

CFP	Connector				CMS Isolation	Module	
PIN	FUNCTION		PIN	PIN	FUNCTION	1,100,010	SOURCE
1	PDM Addr 1	ΙN	1	2	PDM Addr 1	OUT	CEX - 4
2	PDM Addr 2	ΙN	3	4	PDM Addr 2	OUT	CEX - 3
3	PDM Addr 3	ΙN	5	6	PDM Addr 3	OUT	CEX - 2
4	PDM Addr 4	ΙN	7	8	PDM Addr 4	OUT	CEX - 1
5	PXDA Addr 1	ΙN	9	10	PXDA1 Select	OUT	CEA - 9
6	PXDA Addr 2	ΙN	11	12	PXDA2 Select	OUT	CEB - 9
7	PXDA Addr 3	ΙN	13	14	PXDA3 Select	OUT	CEC - 9
8	NA		15	16	PXDA4 Select	OUT	CED - 9
9	NA		17	18	PXDA5 Select	OUT	CEE - 9
10	Data 1	ΙN	19	20	Data 1	OUT	CEX - 15
11	Data 2	ΙN	21	22	Data 2	OUT	CEX - 14
12	Data 3	ΙN	23	24	Data 3	OUT	CEX - 13
13	Data 4	ΙN	25	26	Data 4	OUT	CEX - 12
14	Data 5	ΙN	27	28	Data 5	OUT	CEX - 11
15	Data 6	ΙN	29	30	Data 6	OUT	CEX - 10
16	Data 7	ΙN	31	32	Data 7	OUT	CEX - 22
17	Data 8	ΙN	33	34	Data 8	OUT	CEX - 21
18	CLOCK	ΙN	35	36	CLOCK	OUT	CEX - 20
19	ENABLE	ΙN	37	38	ENABLE	OUT	CEX - 19
20	CLEAR	ΙN	39	40	CLEAR	OUT	CEX - 18
21	Dim Control	ΙN	41	42	Dim Control	OUT	CEX - 17
22	NA		43	44	NA		
23	NA		45	46	NA		
24	NA		47	48	NA		
25	NA		49	50	NA		
26	NA		51	52	NA		
27	PXDA LATCH	OUT	53	54	PXDA 1 LATCH	ΙN	CEA - 24
28	TEST REQUEST		55	56	PXDA 2 LATCH	ΙN	CEB - 24
29	NA		57	58	PXDA 3 LATCH	ΙN	CEC - 24
30	NA		59	60	PXDA 4 LATCH	ΙN	CED - 24
31	NA		61	62	PXDA 5 LATCH	ΙN	CEE - 24
32	NA		63	64	NA		
33	NA						
34	EXT +12 VDC		65	66	CMS +24 VDC		TB 3 / 1
35	EXT -12 VDC		67	68	CMS +24 VDC		TB 3 / 1
36	EXT VDC GND		69	70	CMS VDC GND		TB 3 / 3
37	EXT VDC GND		71	72	CMS VDC GND		TB 3 / 3

NOTE: 1. The CMS Isolation Module's PC socket connector pin location shall be positioned as viewed from the PDA No.4 front. 2. The CMS Isolation Module's card connector shall be keyed between pins 8 and 9.

DEFINITIONS: PDM Addr 1 - Pixel Driver Module Address Line 1.

PXDA Addr 1 - Pixel Driver Assembly Address Line 1.
CEX - 6 - Assigned to all 5 CE Connectors Pin 6.
TB 3 / 1 - Terminal Block 3 Position No.1.

Dim Control - Dimmer Control.

PXDA 1 Select - PXDA No.1 Select Line.
I N - Into Module and/or Connector.
OUT - Out from Module and/or Connector.

PDA NO.4

CMS CE(A to E) and TERMINAL BLOCK CONNECTOR ASSIGNMENTS

CEP	CON	INITA		DC
CEE	COD		w	KO

PIN	FUNCTIO	N	SOURCE	PIN	FUNCTION	SOURCE
	DD1 (+ 11		100.0	10	EMADAE	100 20
4	PDM Addr	1	ISO - 2	19	ENABLE	ISO - 38
3	PDM Addr	2	ISO - 4	18	CLEAR	ISO - 40
2	PDM Addr	3	ISO - 6	17	DIM CONTROL	ISO - 42
1	PDM Addr	4	ISO - 8	22	NA	
9	PXDA SE	LECT	ISO - 10 to 18	23	NA	
6	NA			24	NA	
7	NA			25	NA	
8	NA			26	NA	
9	NA			24	*PXDA 1 LATCH	ISO - 54
15	DATA	1	ISO - 20	24	*PXDA 2 LATCH	ISO - 56
14	DATA	2	ISO - 22	24	*PXDA 3 LATCH	ISO - 58
13	DATA	3	ISO - 24	24	*PXDA 4 LATCH	ISO - 60
12	DATA	4	ISO - 26	24	*PXDA 5 LATCH	ISO - 62
11	DATA	5	ISO - 28	32	NA	
10	DATA	6	ISO - 30	33	NA	
22	DATA	7	ISO - 32	34	VDC GND	TB 3 / 3
21	DATA	8	ISO - 34	35	VDC GND	TB 3 / 3
20	CLOCK		ISO - 36	36	+ 24 VDC	TB 3 / 1
				37	+ 24 VDC	TB 3 / 1

TERMINAL BLOCKS

TB NO.	POSITION	SOURCE
T2	1	NA
T2	2	NA
T2	3	NA
T2	4	NA
T2	5	AC-
T2	6	FAN
T2	7	CB EQUIP.
T2	8	EQ. GND BUS
T2	9	MAIN
T2	10	AC-
T1 & T4	1 to 10	NA
T3	1 to 2	+ 24 VDC P.S.
T3	3 to 4	VDC GND P.S.

NOTE: *Each Latch Line is a decoded line from the CMS ISO MOD and each line shall be connected to the appropriate CE Connector Pin per PXDA Assignment.

DEFINITIONS :	ISO - A	-	CMS Isolation Module Connector Pin A.
	PXDA SELECT	-	PXDA Select Line.
	PDM Addr 1	-	Lamp Driver Module Address Line 1.
	P.S.	-	Model 206 Power Supply.
	TB 3 / 1	-	Terminal Block 3 Position 1.
	CB EOUIP	_	Equipment Circuit Breaker

PIXEL DRIVER MODULE CONNECTOR ASSIGNMENTS

PIN	FUNCTION	SOURCE	PIN	FUNCTION	SOURCE
1	PDM Addr 1	CDX - 4	A	DATA 1	CDX -15
2	PDM Addr 2	CDX - 3	В	DATA 2	CDX -14
3	PDM Addr 3	CDX - 2	C	DATA 3	CDX -13
4	PDM Addr 4	CDX - 1	D	DATA 4	CDX -12
5	PXDA SELECT	CDX - 9	E	DATA 5	CDX -11
6	I.D. SLOT 1	PXDAM	F	DATA 6	CDX -10
7	I.D. SLOT 2	PXDAM	Н	DATA 7	CDX -22
8	I.D. SLOT 3	PXDAM	J	DATA 8	CDX -21
9	I.D. SLOT 4	PXDAM	K	CLOCK	CDX -20
10	DC GND	CDX -34 to 35	L	ENABLE	CDX -19
11	NA		M	PXDA LATCH	CDX -24
12	NA		N	CLEAR	CDX -18
13	NA		P	+ 24 VDC	CDX -36 to 37
14	NA		R	DC GND	CDX -34 to 35
15	NA		S	DIM CONTROL	CDX -17
16	NA		T	NA	
17	NA		U	NA	
18	AC + 1	PXDA TB 1-1	V	AC + 1	PXDA TB 1-1
19	AC + 1	PXDA TB 1-1	W	AC + 1	PXDA TB 1-1
20	AC + 2	PXDA TB 1-2	X	AC + 2	PXDA TB 1-2
21	AC + 2	PXDA TB 1-2	Y	AC + 2	PXDA TB 1-2
22	AC + 3	PXDA TB 1-3	Z	AC + 3	PXDA TB 1-3
23	AC + 3	PXDA TB 1-3	AA	AC + 3	PXDA TB 1-3
24	CCS 1	P1-1	BB	CCS21	P3-5
25	CCS 2	P1-2	CC	CCS22	P3-6
26	CCS 3	P1-3	EE	CCS23	P3-7
27	CCS 4	P1-4	FF	CCS24	P3-8
28	CCS 5	P1-5	HH	CCS25	P4-1
29	CCS 6	P1-6	JJ	CCS26	P4-2
30	CCS 7	P1-7	KK	CCS27	P4-3
31	CCS 8	P1-8	LL	CCS28	P4-4
32	CCS 9	P2-1	MM	CCS29	P4-5
33	CCS 10	P2-2	NN	CCS30	P4-6
34	CCS 11	P2-3	PP	CCS31	P4-7
35	CCS 12	P2-4	RR	CCS32	P4-8
36	CCS 13	P2-5	SS	CCS33	P5-1
37	CCS 14	P2-6	TT	CCS34	P5-2
38	CCS 15	P2-7	UU	CCS35	P5-3
39	CCS 16	P2-8	VV	CCS36	P5-4
40	CCS 17	P3-1	WW	CCS37	P5-5
41	CCS 18	P3-2	XX	CCS38	P5-6
42	CCS 19	P3-3	YY	CCS39	P5-7
43	CCS 20	P3-4	ZZ	CCS40	P5-8

NOTES: 1. The Pixel Driver Module Connector shall be a 43/86 Edge Connector.

2. AC+ 1, AC+ 2 and AC+ 3 and associated AC- Bus of T.B. A1 & 2 shall be routed to associated PDM connectors, T.B. A1 to PDM connectors 7 to 12 and T.B. A2 to PDM connectors 1 to 6.

DEFINITIONS: PDM Addr 1 - Pixel Driver Module Address Line 1.

PXDA SELECT - PXDA Address Enable Line.

PXDAM - PXDA Motherboard Network.

CDX - 1 - Assigned to all 5 CD Connectors (A to E) Pin 1.

CCS 1 - CC Socket Connector Pin 1.

P1-1 - Pixel 1-1 (As designated from Detail 8-7-14).

PXDA TB 1-2 - PXDA Terminal Block 1 Position 2.

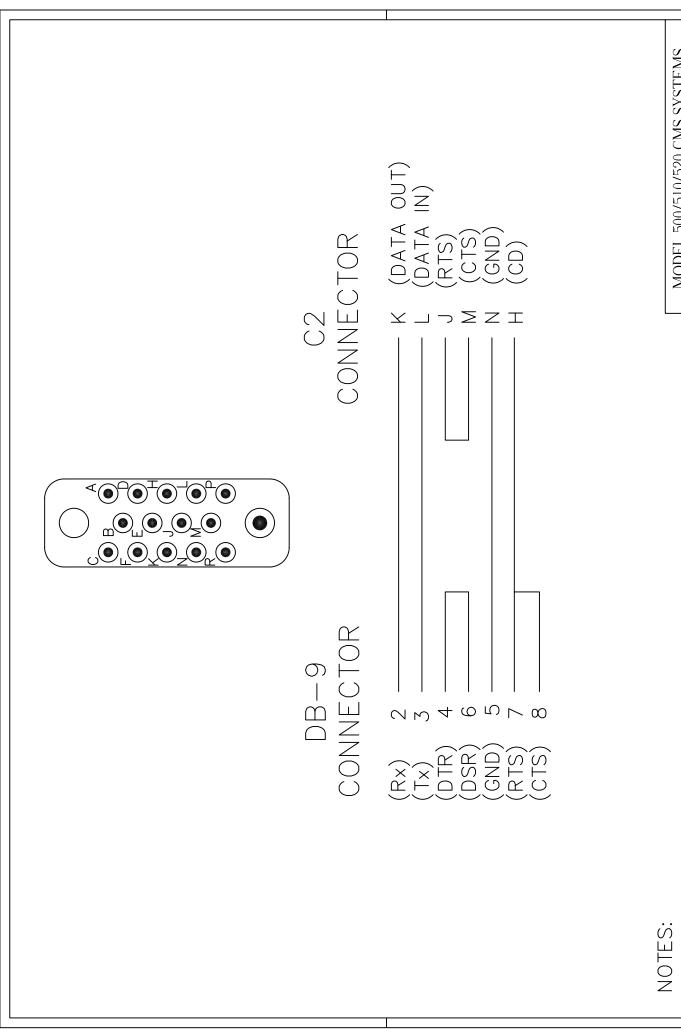
CONTROLLER ISOLATION ASSEMBLY CONNECTOR PIN ASSIGNMENTS FOR C8 AND C9 CONNECTORS

CONNECTOR	PIN	FUNCTION	CONNECTOR	PIN	FUNCTION
C8	1	PDM Addr 1	С9	1	CLOCK
C8	2	Ground	C9	2	Ground
C8	3	PDM Addr 2	C9	3	ENABLE
C8	4	Ground	C9	4	Ground
C8	5	PDM Addr 3	C9	5	CLEAR
C8	6	Ground	C9	6	Ground
C8	7	PDM Addr 4	C9	7	Dim Control Line
C8	8	Ground	C9	8	Ground
C8	9	PXDA Addr 1	C9	9	NA
C8	10	Ground	C9	10	NA
C8	11	PXDA Addr 2	C9	11	NA
C8	12	Ground	C9	12	NA
C8	13	PXDA Addr 3	C9	13	LATCH
C8	14	Ground	C9	14	Ground
C8	15	NA	C9	15	TEST
C8	16	NA	C9	16	Ground
C8	17	NA	C9	17	NA
C8	18	NA	C9	18	NA
C8	19	Data 1	C9	19	NA
C8	20	Ground	C9	20	NA
C8	21	Data 2	C9	21	NA
C8	22	Ground	C9	22	NA
C8	23	Data 3	C9	23	NA
C8	24	Ground	C9	24	NA
C8	25	Data 4	C9	25	NA
C8	26	Ground	C9	26	NA
C8	27	Data 5	C9	27	NA
C8	28	Ground	C9	28	NA
C8	29	Data 6	C9	29	NA
C8	30	Ground	C9	30	NA
C8	31	Data 7	C9	31	NA
C8	32	Ground	C9	32	NA
C8	33	Data 8	C9	33	NA
C8	34	Ground	C9	34	EXT +12 VDC
C8	35	NA	C9	35	EXT - 12 VDC
C8	36	NA	C9	36	Ground
C8	37	NA	C9	37	Ground

DEFINITIONS: Ground - DC Ground.

PDM Addr 1 - Pixel Driver Module Address Line 1. PXDA Addr 1 - Pixel Driver Assembly Address Line 1.

NOTE: C8 keyed at pin 17, and C9 keyed at pin 12.

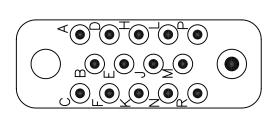


Direct Mode DB9 Communications Cable Between Laptop and 170 Controller MODEL 500/510/520 CMS SYSTEMS

Pins Not Shown Are Not Connected.

FEBRUARY 2003

No Scale



C2 CONNECTOR

DB-9 CONNECTOR

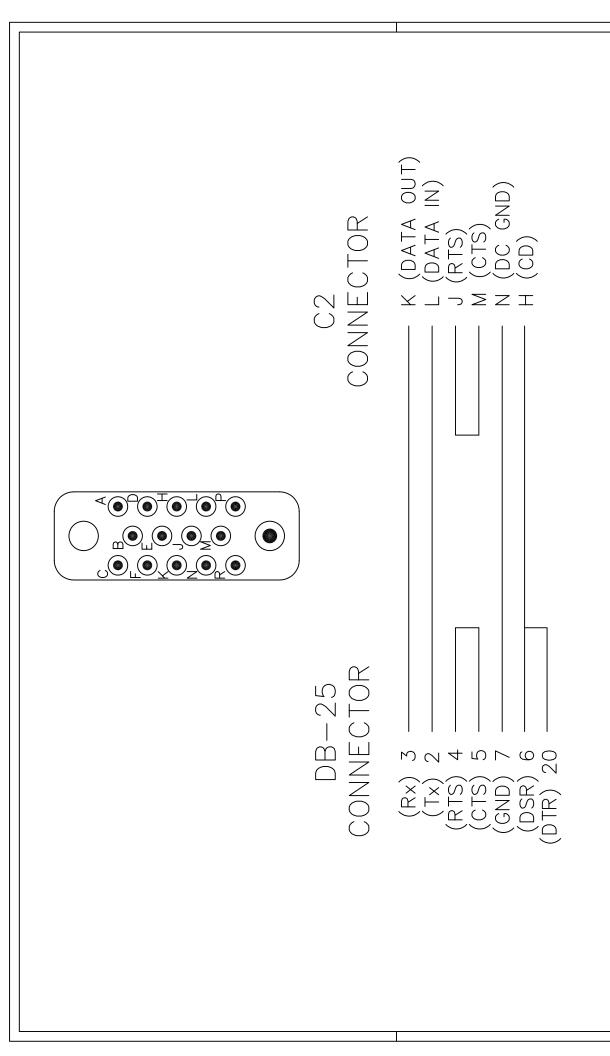
K (DATA OUT)
L (DATA IN)
J (RTS)
M (CTS)
N (GND)
H (CD) (RX) 2 (TX) 3 (RTS) 7 (CTS) 8 (GND) 5 (CDC) 1 (CDC)

NOTES: Pins Not Shown Are Not Connected.

fitle Modem DB9 Communications Cable

Between Modem and 170 Controller NOVEMBER 2002

MODEL 500/510/520 CMS SYSTEMS



MODEL 500/510/520 CMS SYSTEMS

Direct Mode DB-25 Communications Cable

Pins Not Shown Are Not Connected.

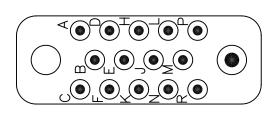
NOTES:

Between Computer and 170 Controller

FEBRUARY 2003

No Scale

8-7-32



CONNECTOR

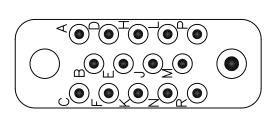
DB-25 Connector

K (DATA OUT)
L (DATA IN)
J (RTS)
M (CTS)
N (DC GND)
H (CD) (RX) 3 (TX) 2 (RTS) 4 (CTS) 5 (GND) 7 (CDC) 8

NOTES: Pins Not Shown Are Not Connected.

FEBRUARY 2003

No Scale



C20 or C2 CONNECTOR

DB-9 CONNECTOR L (DATA C (DATA I (CTS) N (GND) H (CD) (Rx) 2 – (Tx) 3 – (CTS) 8 – (GND) 5 – (CDC) 1 –

NOTES: Pins Not Shown Are Not Connected.

MODEL 500/510/520 CMS SYSTEMS

THE
GDI Modem DB9 Communications Cable
Between GDI Modem and 170 Controller

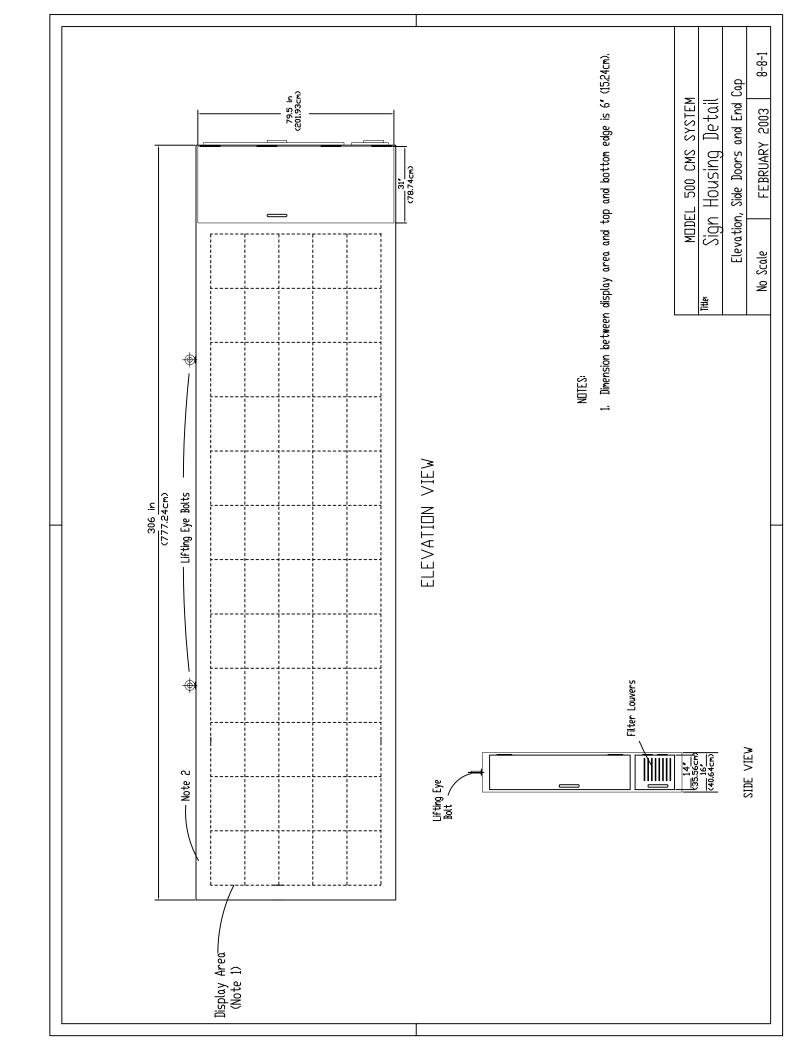
No Scale | FEBRUARY 2003 |

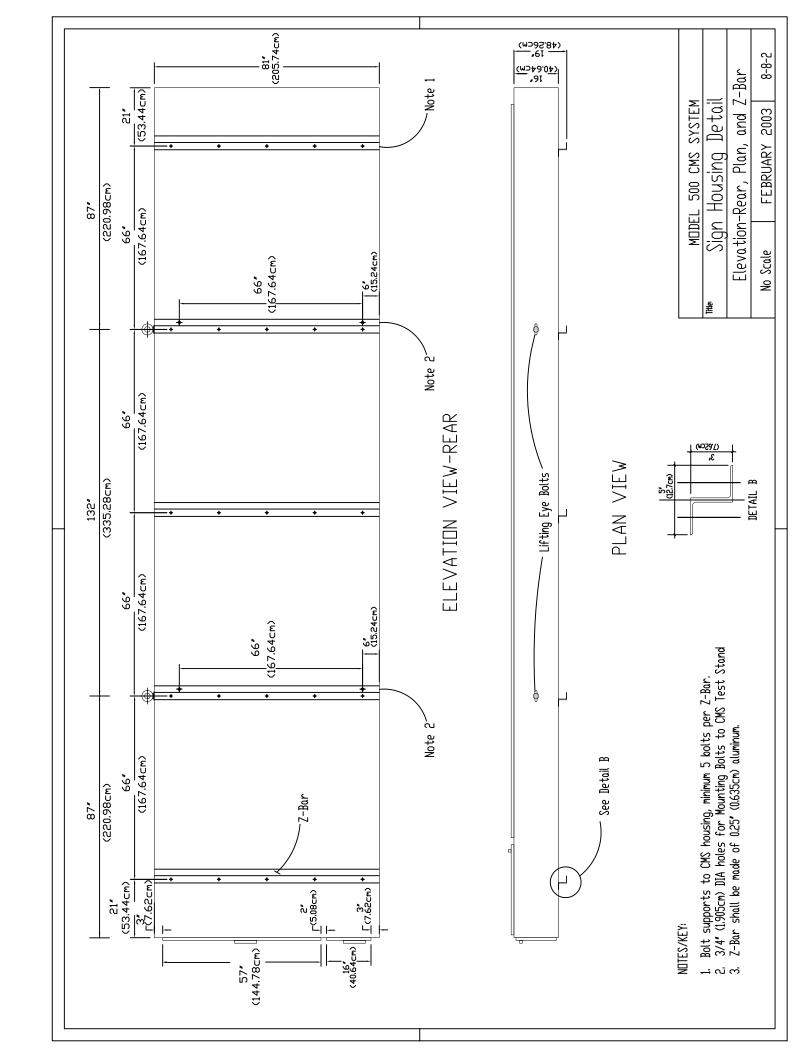
2003 8 - 7 -

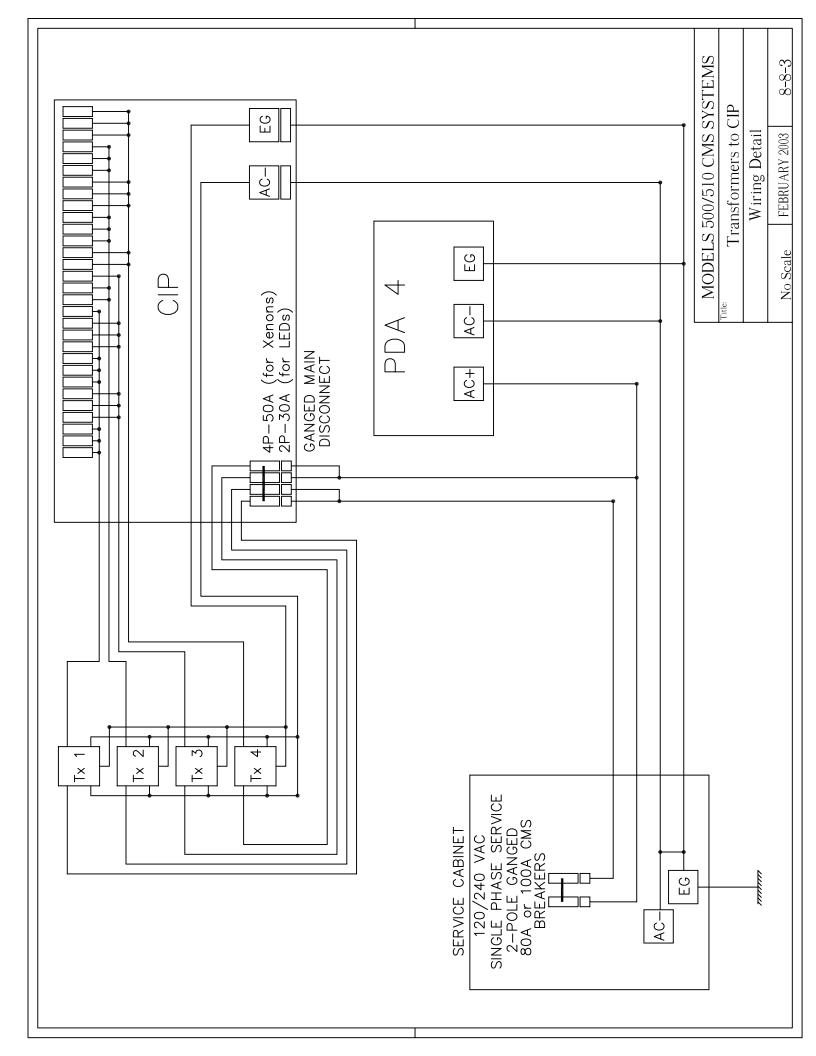
SECTION 8

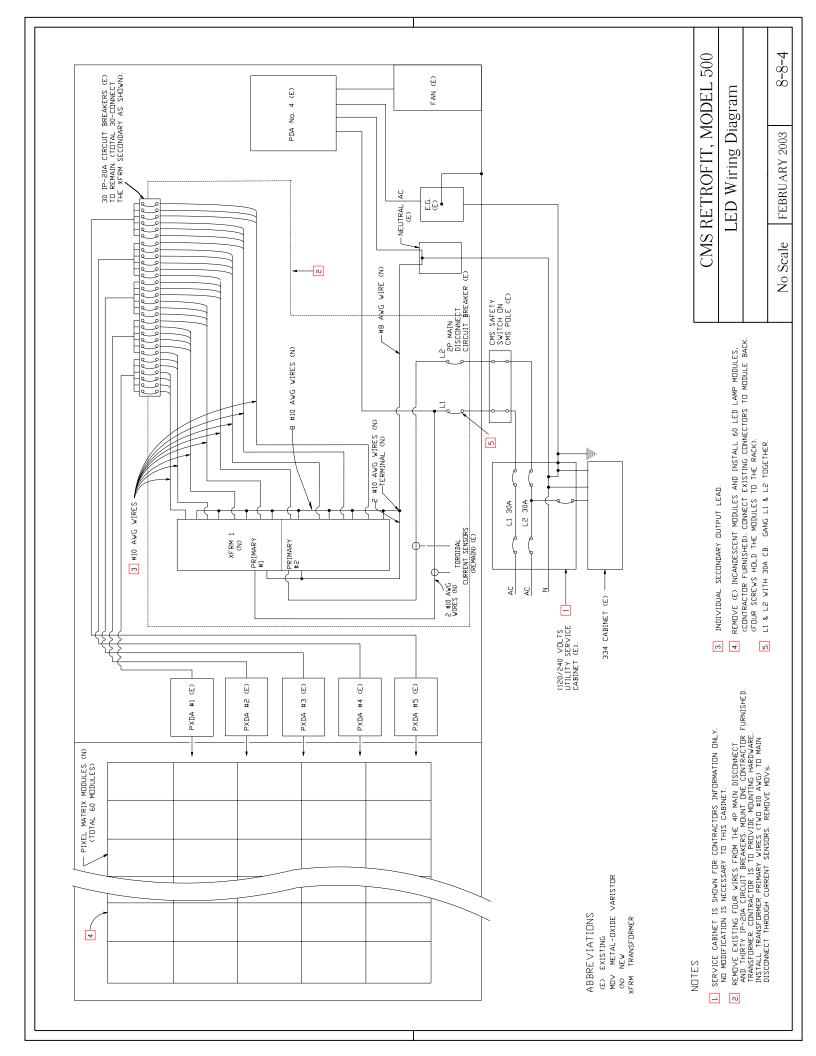
MODELS 500 CMS PLAN DETAILS

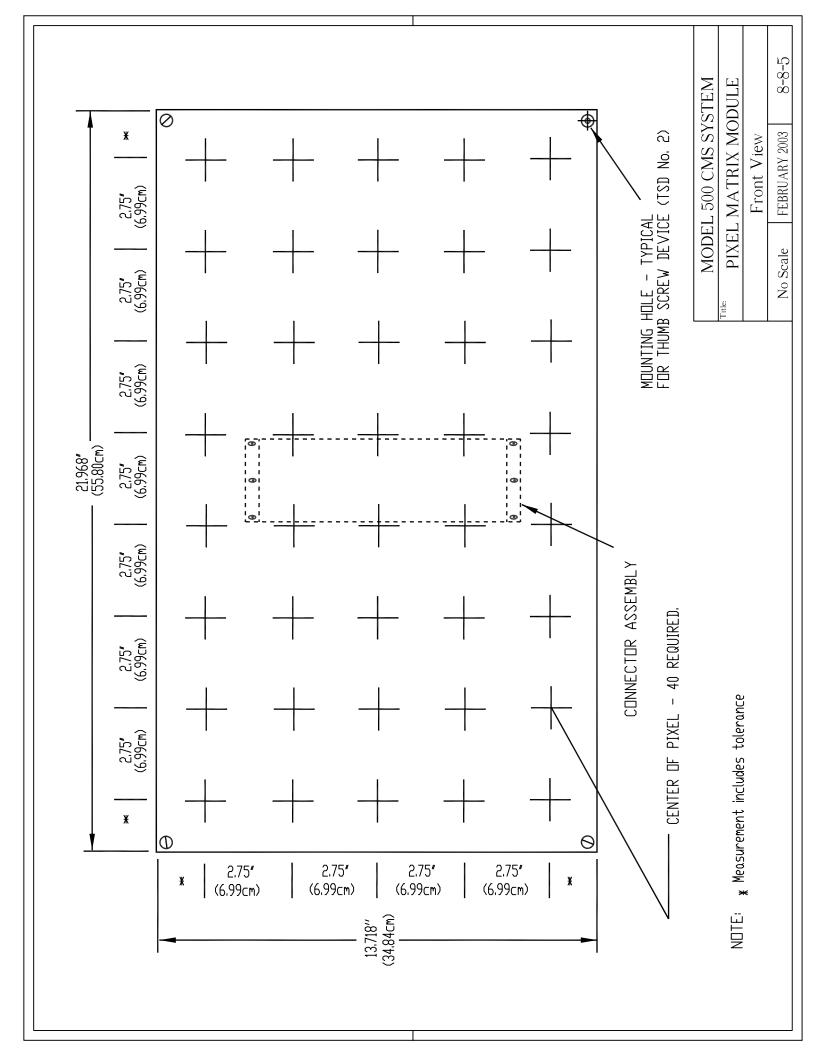
	PLAN
MODEL 500 SIGN HOUSING, FRONT AND SIDE VIEWS	8-8-1
MODEL 500 SIGN HOUSING, REAR AND TOP VIEWS	8-8-2
TRANSFORMERS TO CIP WIRING DETAILS	8-8-3
CMS RETROFIT, LED WIRING DIAGRAM	8-8-4
PIXEL MATRIX MODULE, FRONT VIEW	8-8-5
PIXEL MATRIX MODULE, REAR VIEW	8-8-6
PIXEL MATRIX MODULE, SIDE VIEW, FULL PANEL MOLDING, OPTION 1	8-8-7
PIXEL MATRIX MODULE, SIDE VIEW, FULL PANEL MOLDING, OPTION 2	8-8-8
PIXEL MATRIX MODULE, END VIEW, LED PIXEL, TYPE 1	8-8-9
PIXEL MATRIX MODULE, END VIEW, LED PIXEL, TYPE 2	8-8-10
CMS CONTROL COMPARTMENT MOUNTING DETAIL	8-8-11
CMS TESTING SUPPORT FRAME, ELEVATION VIEW	8-8-12
CMS TESTING SUPPORT FRAME, PLAN VIEW	8-8-13
CMS LIFTING BRAKET	8-8-14
SHIPPING PALLET	8-8-15
PALLETIZED CMS, FRONT VIEW	8-8-16

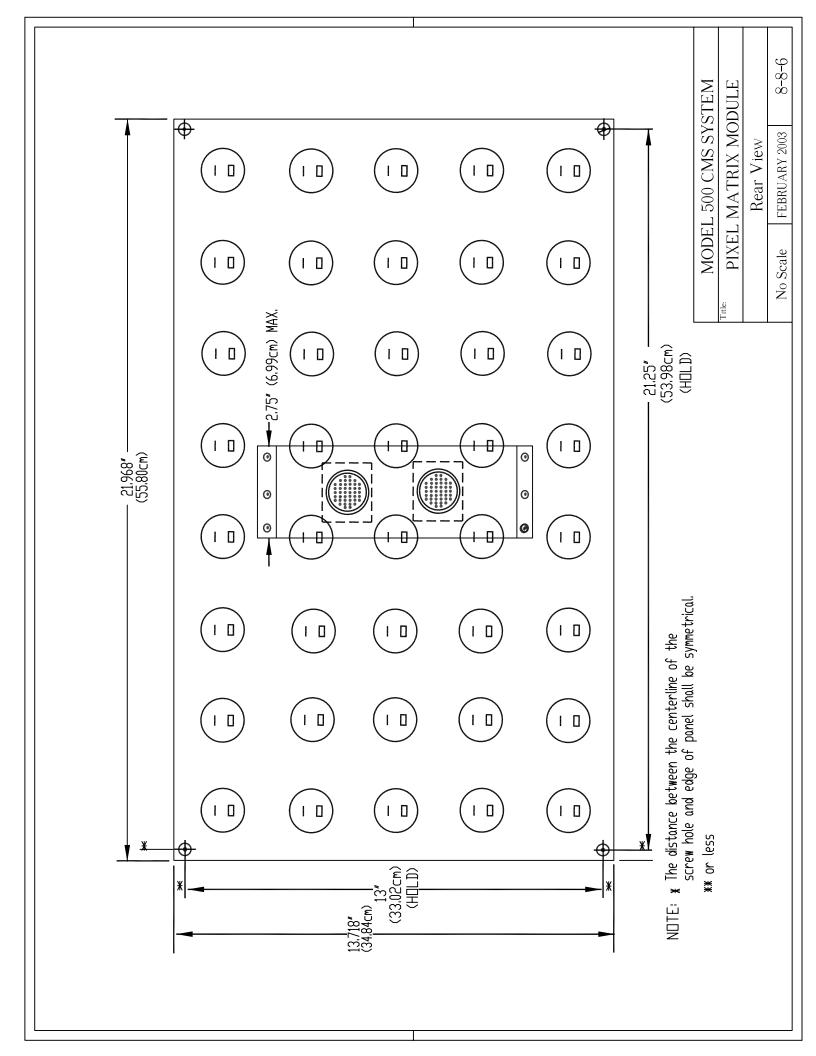


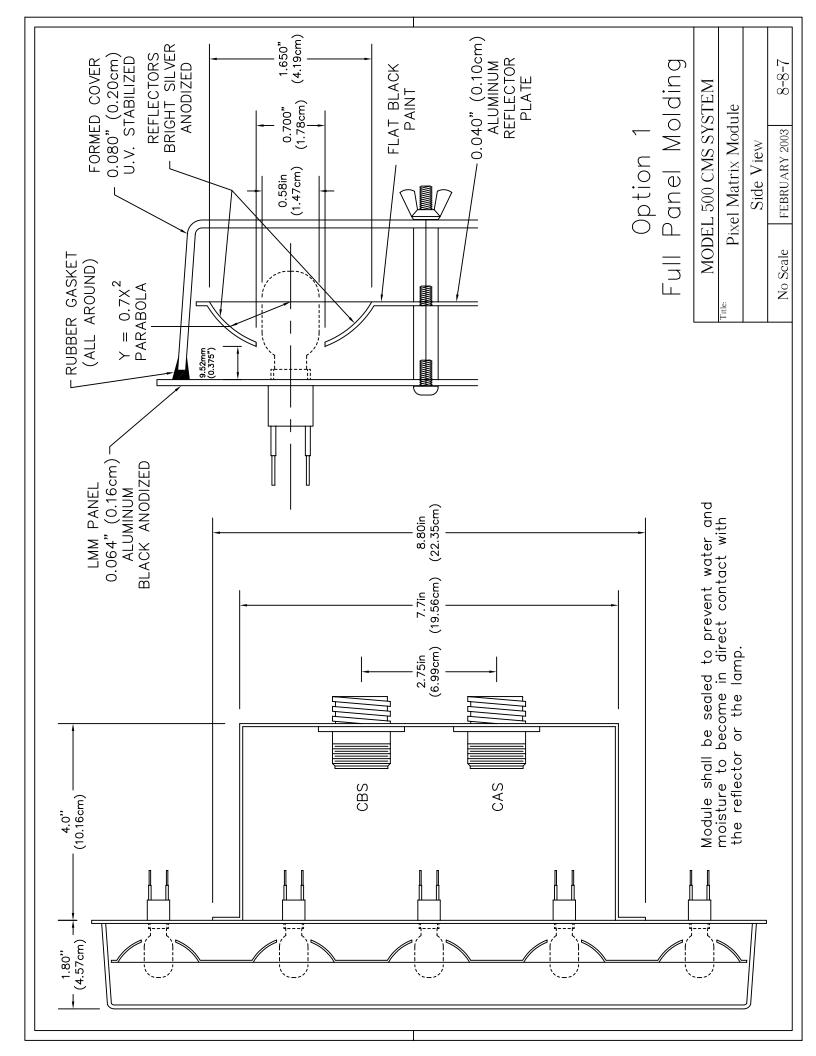


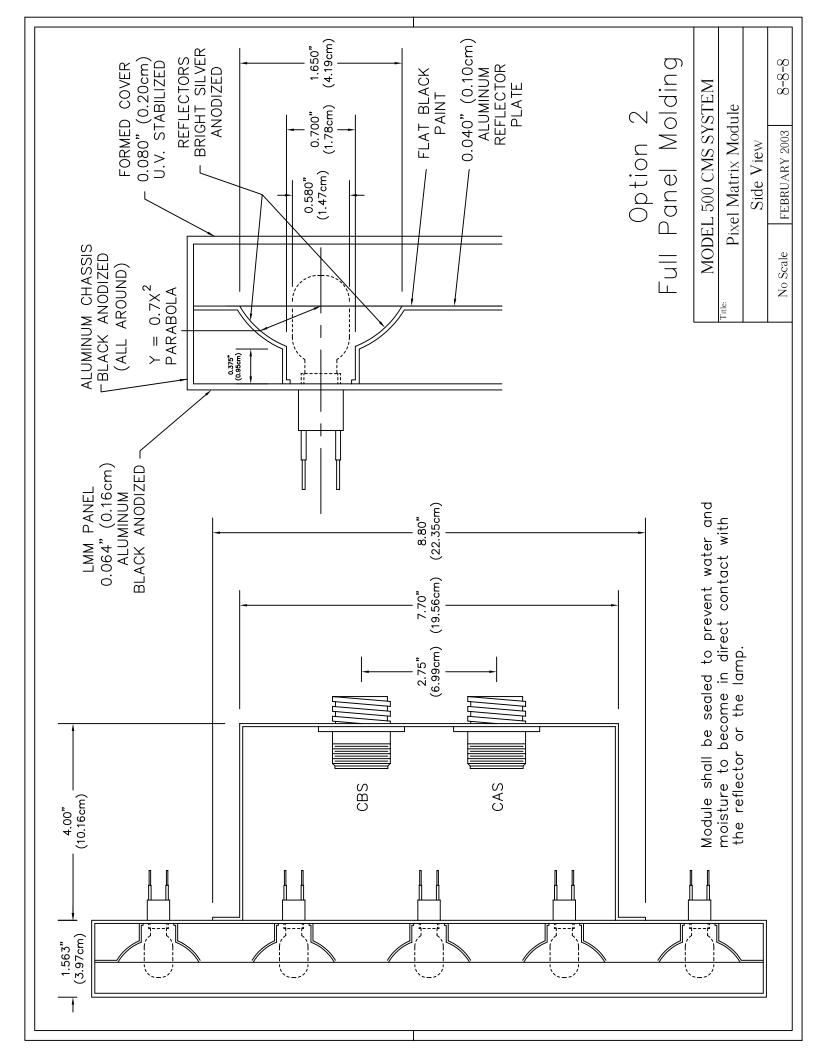


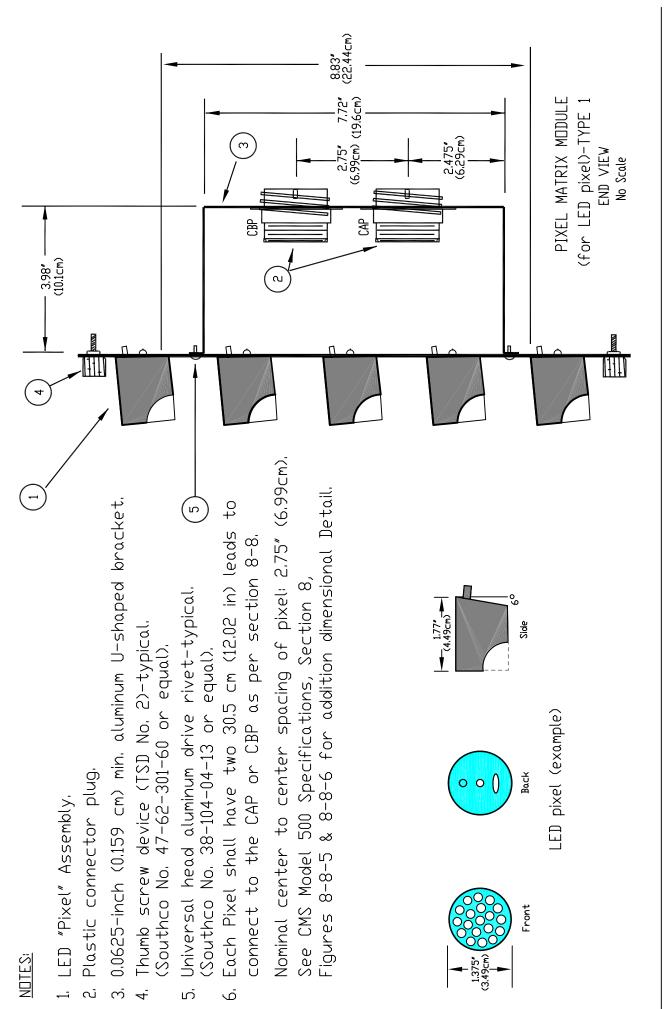




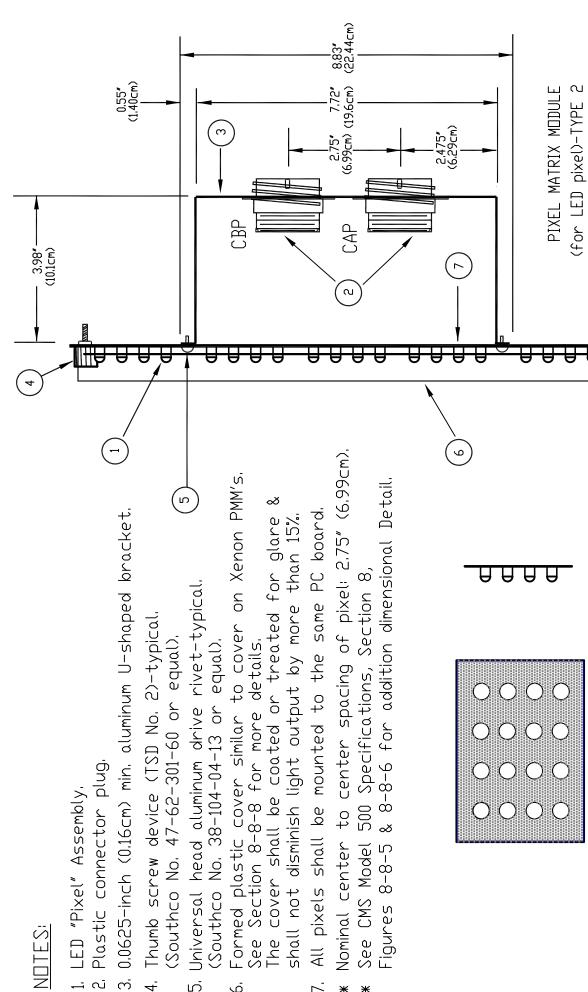








END VIEW No Scale



NOTES

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4

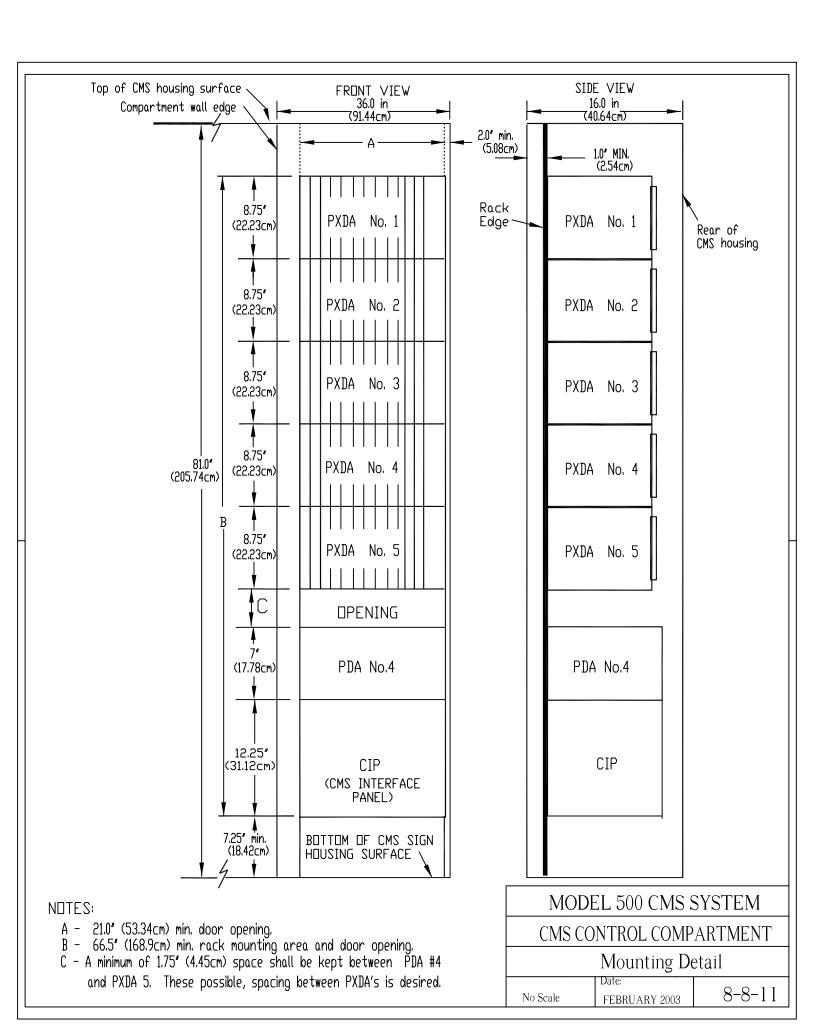
Ŋ

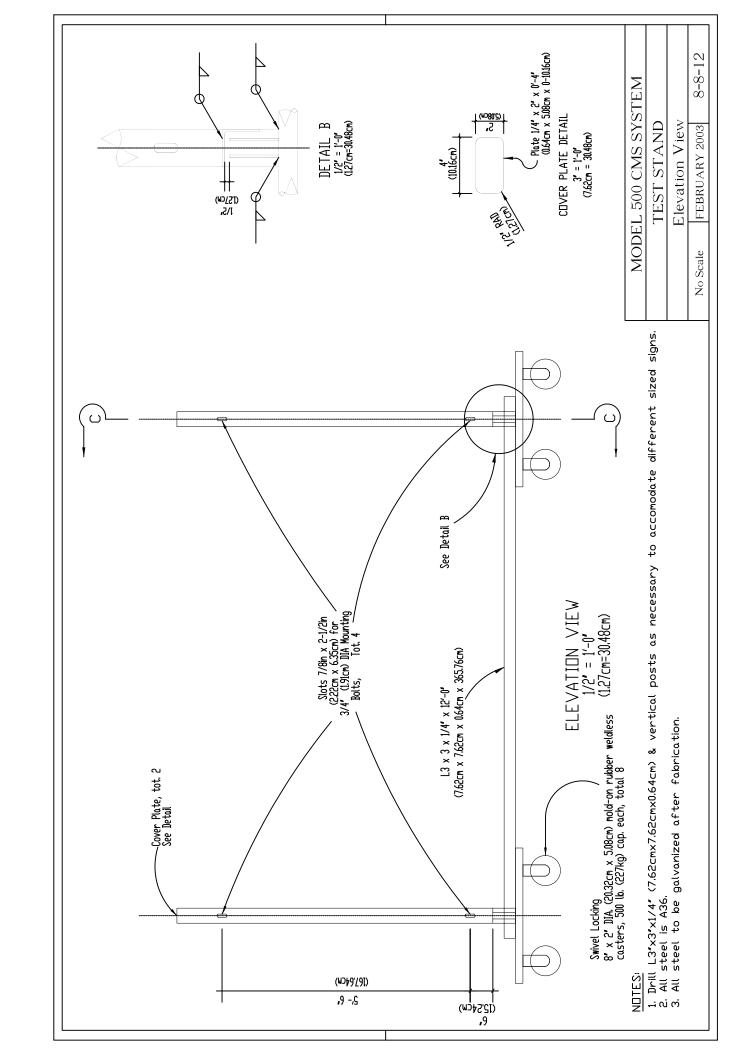
9

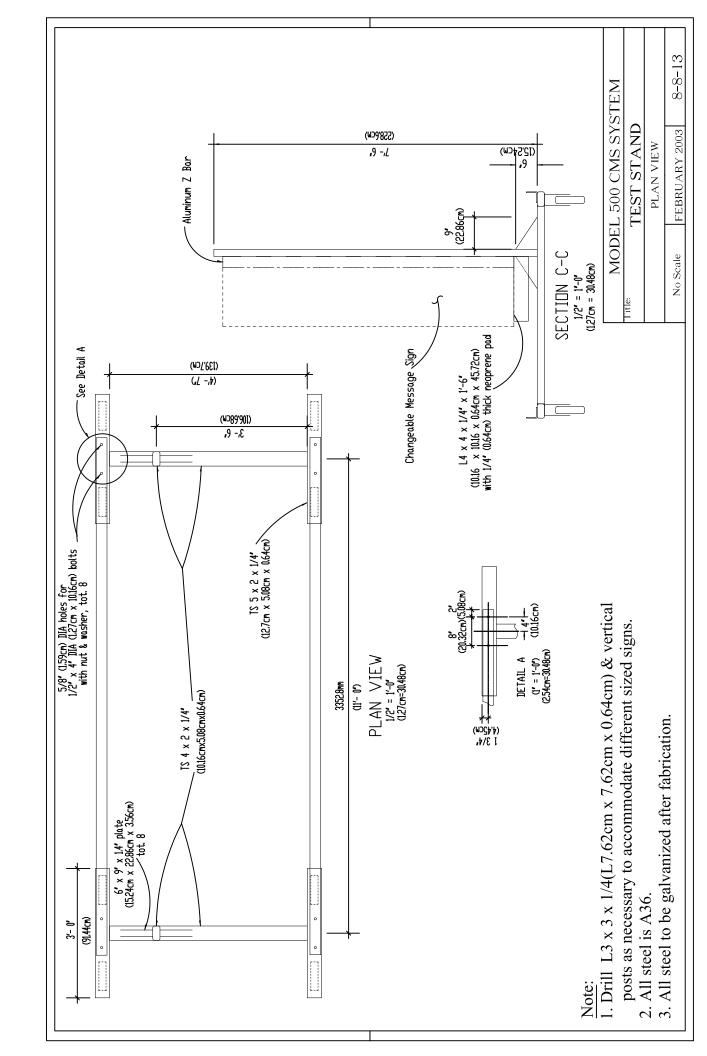
LED Pixel Dimensions and Shape (example)

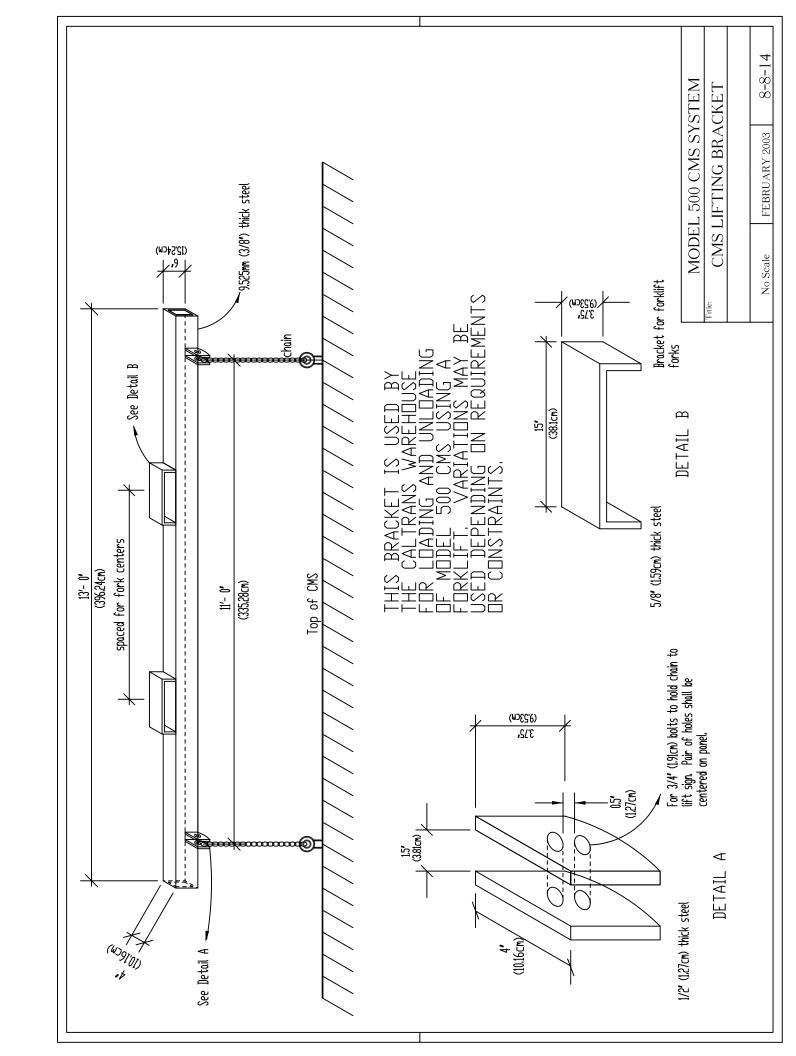
Front

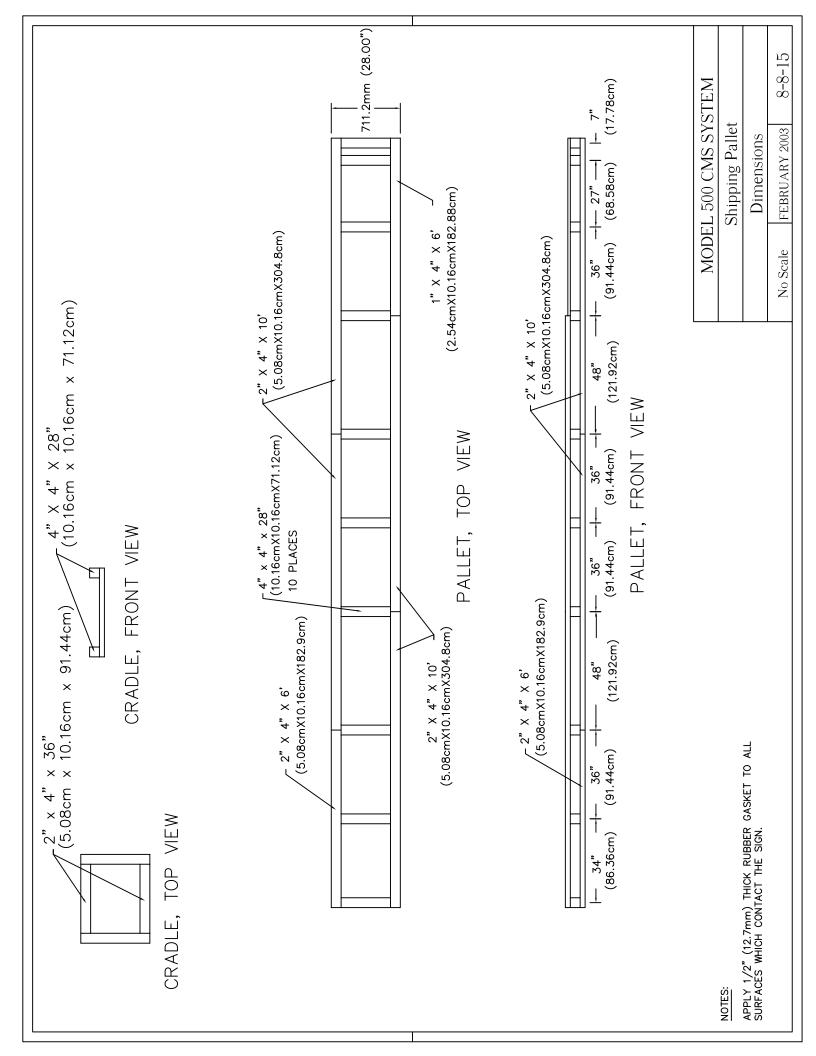
Side

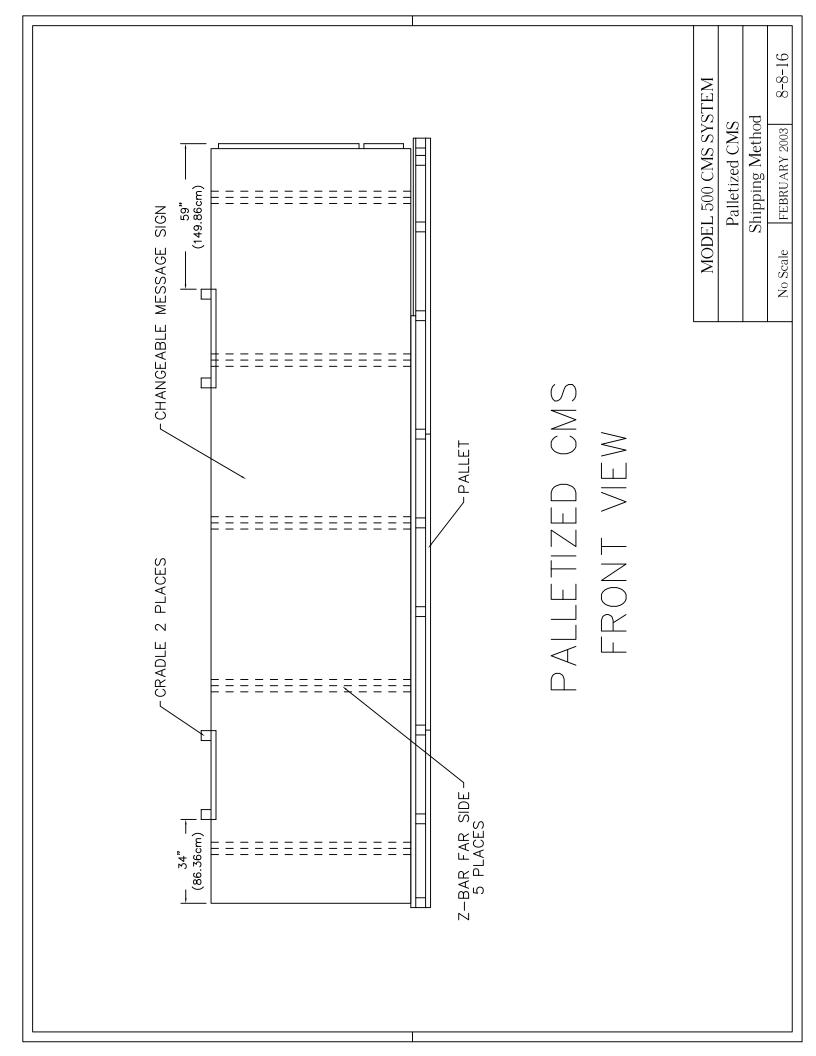










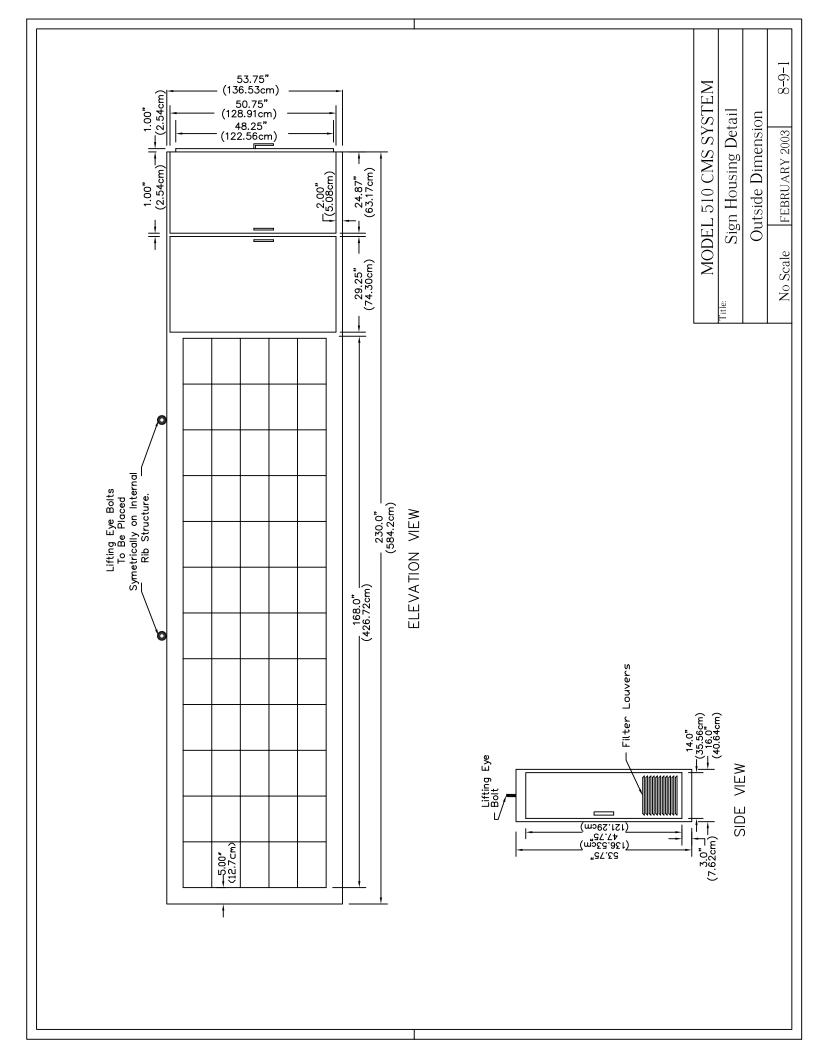


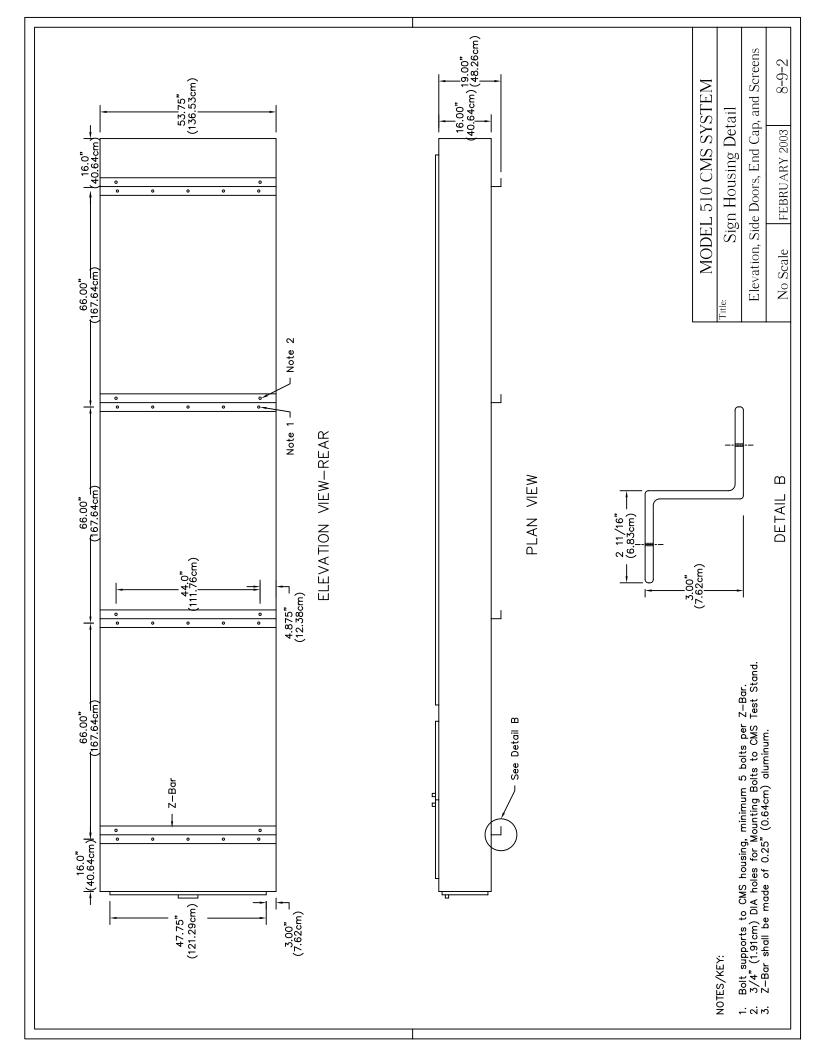
SECTION 9

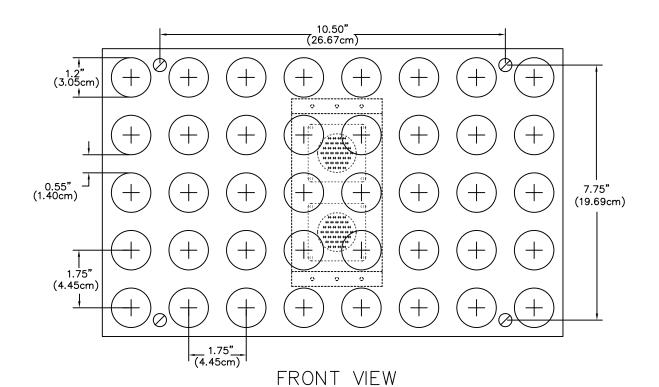
MODELS 510 CMS PLAN DETAILS

	PLAN
MODEL 510 SIGN HOUSING, FRONT AND SIDE VIEWS	8-9-1
MODEL 510 SIGN HOUSING, REAR AND TOP VIEWS	8-9-2
PIXEL MATRIX MODULE, BULB AND THUMBSCREW SPACING	8-9-3
PIXEL MATRIX MODULE, SIDE VIEW, FULL PANEL MOLDING, OPTION 1	8-9-4
PIXEL MATRIX MODULE, SIDE VIEW, FULL PANEL MOLDING, OPTION 2	8-9-5
CMS CONTROL COMPARTMENT MOUNTING DETAIL	8-9-6
CMS LIFTING BRAKET	8-9-7
SHIPPING PALLET DIMENSIONS	8-9-8

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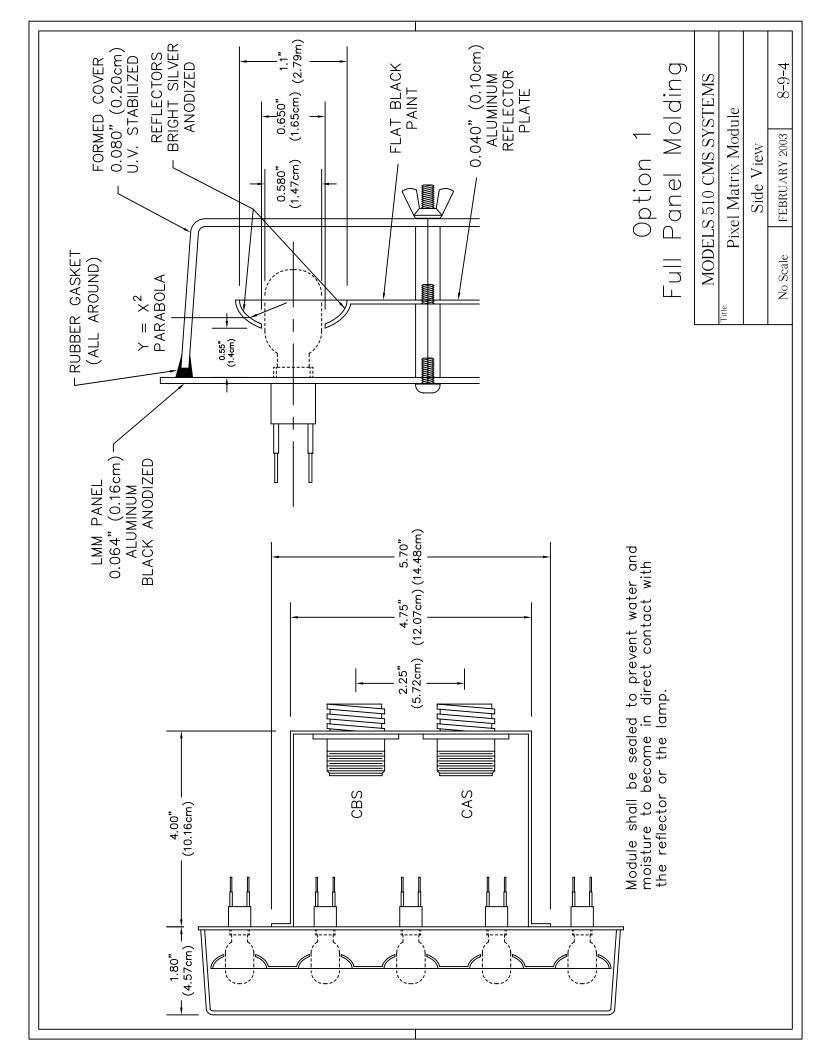
___ 14" ___ (35.56cm) 8.75" (22.23cm)

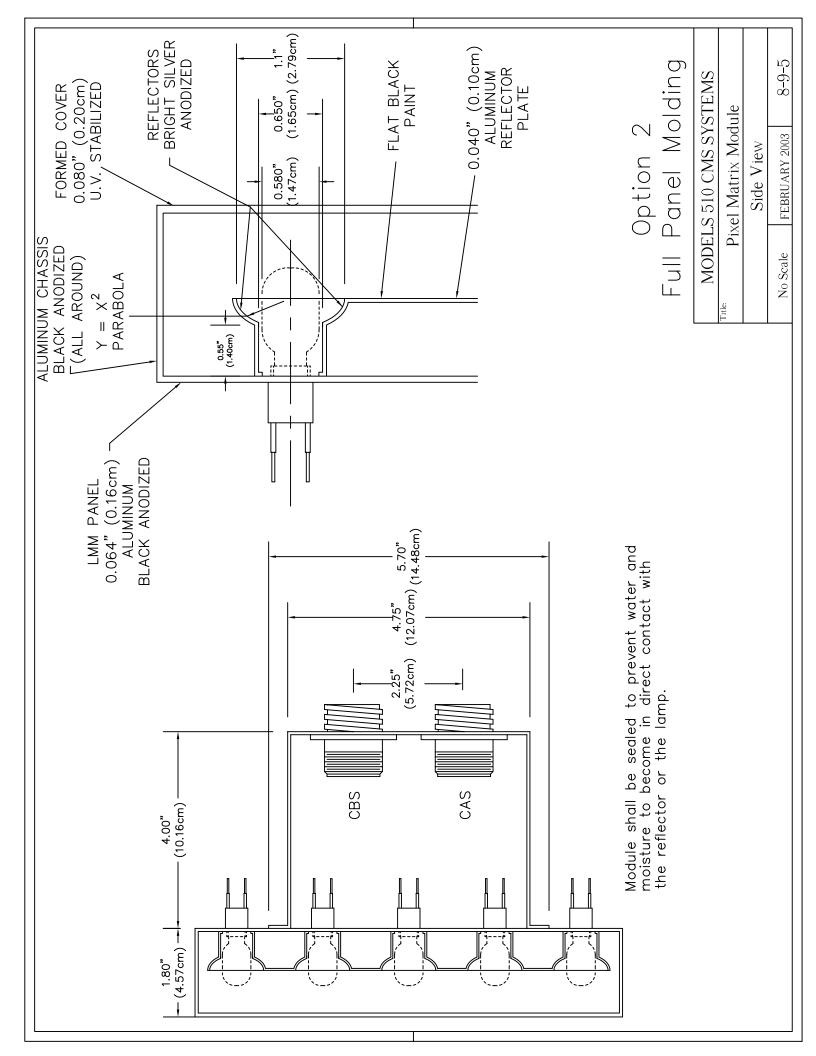
REAR VIEW

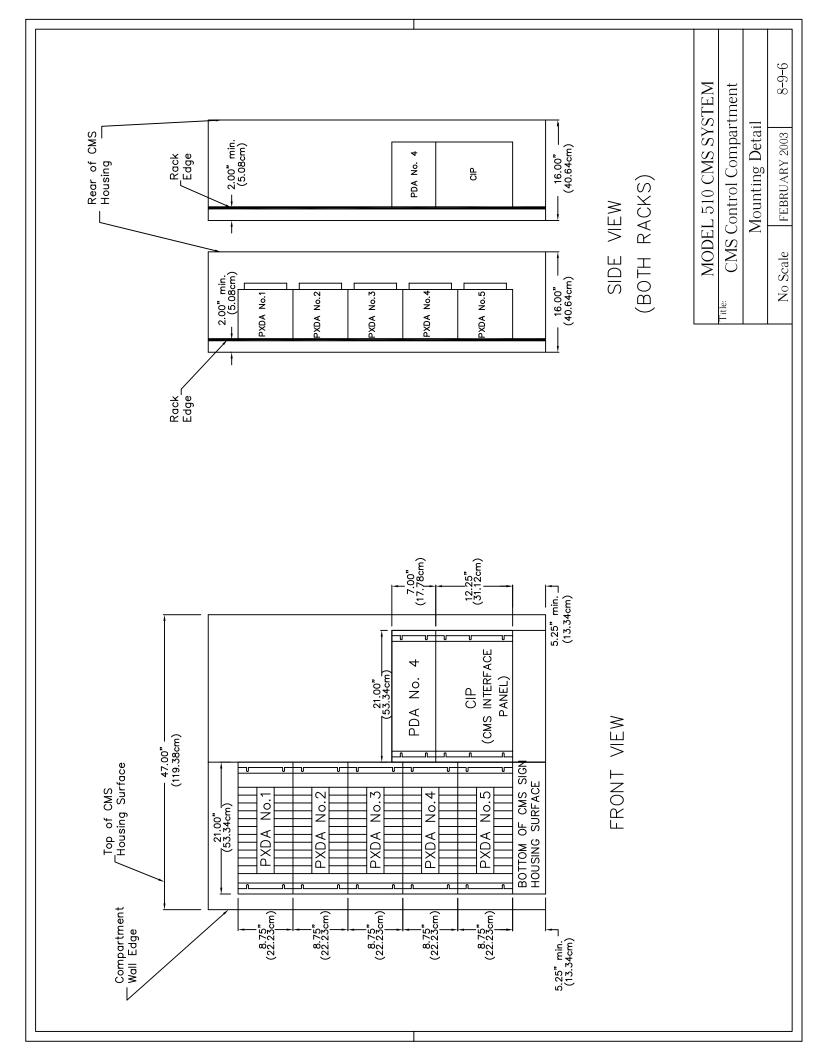
NOTES/KEY:

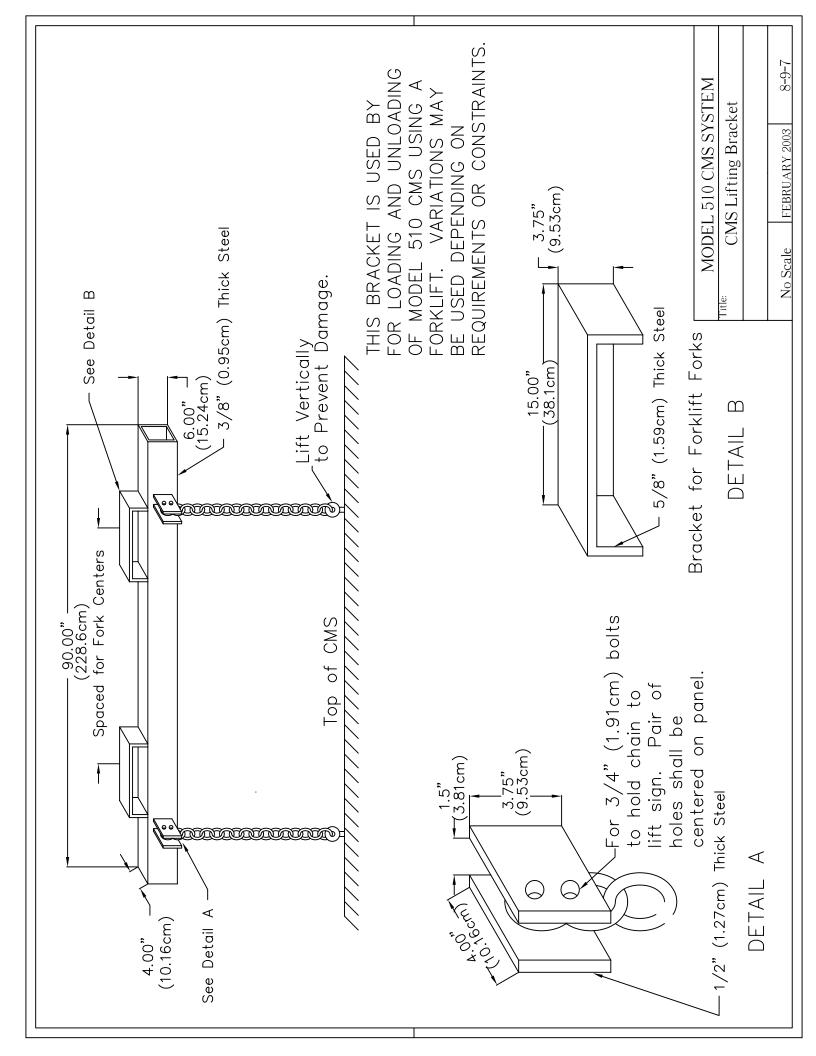
- Outer dimensions include tolerance.
- Center of pixel sockets 40 total.
- Captive thumbscrew device.
- Spacing of thumbscrews within panel shall be symmetrical. Connector support shall be symmetrical within the panel. Pixel array shall be symmetrical within the panel.

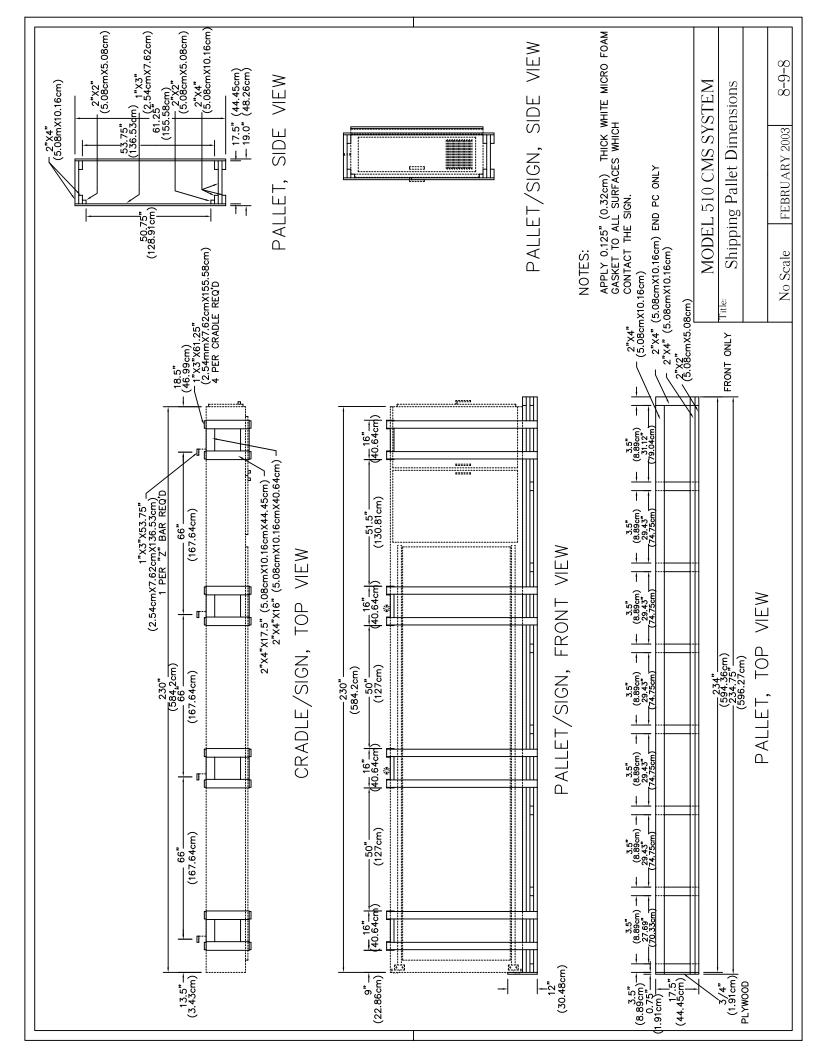
M	ODEI	L 510 CMS SY	STEM	
Title:	Pix	el Matrix Mod	lule	
Bulb and thumbscrew spacing				
No Sca	le	FEBRUARY 2003	8-9-3	











SECTION 10

MODELS 520 CMS PLAN DETAILS

	PLAN
MODEL 520 SIGN HOUSING, FRONT AND SIDE VIEWS	8-10-1
MODEL 520 SIGN HOUSING, REAR AND TOP VIEWS	8-10-2
TRANSFORMERS TO CIP WIRING DETAIL	8-10-3
CMS CONTROL COMPARTMENT MOUNTING DETAIL	8-10-4
PIXEL MATRIX MODULE, FRONT VIEW	8-10-5
PIXEL MATRIX MODULE, REAR VIEW	8-10-6
PIXEL MATRIX MODULE, SIDE VIEW, FUL PANEL MOLDING, OPTION 1	8-10-7
PIXEL MATRIX MODULE, SIDE VIEW, FUL PANEL MOLDING, OPTION 2	8-10-8
CMS SHIPPING PALLET	8-10-9

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